



# MIP

Marketing Innovative Products

**2017/04**

**SMART  
SENSING**

**IOT NODES**

**INDUSTRY 4.0**

**MEMS**

**SENSOR  
FUSION**

**WIRELESS**

**SAFETY**



**EBV**Elektronik  
| An Avnet Company |



## Smarter Nodes for a Smarter IoT

Data is the most important asset in our interconnected world. The more information we are able to collect and analyse the more accurate, effective and successful will decisions and actions based on this data be. In order to stay ahead of the competition and to be innovative in the future we will need to access, collect, process and analyse an increasing amount of relevant information. Bandwidth, processing and analytics power in the cloud as well as energy supply to remote and hard-to-access sensors and IoT nodes are limiting factors that we need to overcome in this process. EBV is able to provide you with design support and technology that will enable you to solve those challenges.

Sophisticated processing technology makes it feasible to add intelligence to the sensor node in order to perform analytics locally and send only relevant data points to the cloud. This strategy is of great advantage if bandwidth, security and latency are critical. The advanced portfolio of low-power and energy harvesting solutions in EBV's linecard further creates the freedom to develop applications that are independent of external power.

In addition, EBV's broad portfolio of long range wireless transceivers and connectivity modules enables the connection of remote nodes to the cloud.

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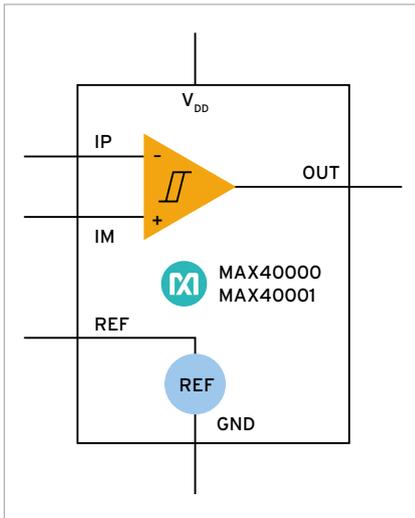
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# MAX40000

## 1.7 V, 0.9 $\mu$ A, Comparators with Built-in Reference



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MAX40000

**EBV presents the MAX40000/  
MAX40001 low power, small  
footprint comparators for a voltage  
threshold detection.**

The MAX40000/MAX40001 are tiny, single comparators with built-in voltage references that are ideal for a wide variety of portable electronics applications, such as cell phones, portable instruments, and notebooks that have extremely tight board space and power constraints. The MAX40000/

MAX40001 are available in a 6-bump wafer-level package (WLP) with  $1.11 \times 0.76 \text{ mm}^2$  footprint and a 6-pin SOT23 package. The MAX40000 has a push-pull output and the MAX40001 has an open-drain output.

The devices offer a supply voltage range from 1.7...5.5 V and consume only 0.9  $\mu$ A of supply current. They also feature internal filtering to provide high RF immunity, important in many portable applications.

The devices have a high-precision integrated reference that is factory trimmed to an initial accuracy of 1 % and better than 2.5 % over the entire temperature range. Internal reference voltage options include 1.252 V, 1.66 V, 1.94 V, and 2.22 V. See Ordering Information for help with ordering a MAX40000/MAX40001 with a particular voltage reference value and package type. The reference output is stable for capacitive loads up to 100 pF.

These devices are fully specified over  $-40...+125 \text{ }^\circ\text{C}$  automotive temperature range.

### KEY FEATURES

- Micropower Operating Current (0.9  $\mu$ A typ., 1.7  $\mu$ A max.) Preserves Battery Power
- Tiny  $1.11 \times 0.76 \text{ mm}^2$  6-Bump WLP and SOT23 Packages Save Board Space
- Internal Precision Reference Saves Space and Cost of an External Reference
  - <1 % at Room Temperature, <2.5 % Over Temp Reference
  - Multiple Reference Voltages (1.252 V, 1.66 V, 1.94 V, and 2.22 V)
- Input Voltage Range =  $-0.2...5.7 \text{ V}$
- Supply Voltage Range (1.7...5.5 V) Allows Operation from 1.8 V, 2.5 V, 3 V, and 5 V Supplies
- <10  $\mu$ s Propagation Delay
- Push-Pull (MAX40000) or Open-Drain (MAX40001) Outputs

### APPLICATION EXAMPLES

- Cell Phones
- Electronic Toys
- Level Detectors
- Notebook Computers
- Portable Medical Instruments/Wearables
- Tablets and Consumer Accessories

## TB67S508FTG

### Stepper Motor Driver, ACDS (Advanced Current Detect System), 40 V/3 A



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Toshiba TB67S508FTG

#### Latest-Generation Stepper-Motor Controller from Toshiba saves Space and Ensures Cooler, Quieter Running.

Toshiba has begun sampling a new 40 V/3.0 A stepper motor control IC that requires no external current-sense resistors. The Advanced Current Detection System (ACDS), integrated into the new 5 × 5 mm TB67S508FTG stepper-motor controller eliminates the effects of resistor-tolerance errors, ensuring greater accuracy (+/-5 %) and uniformity. Compared to the typical 7 × 7 mm control IC with external resistors the TB67S508FTG saves component costs and minimizes footprint by using 66 % of the board real-estate only.

In addition, the built-in power stage leverages the latest DMOS technology to boost energy efficiency, which aids space-saving design and eases thermal management by reducing internal heat generation. The transistors of the output bridge have very low on-resistance ( $R_{DS(ON)}$ ) of 0.45 Ω (typical, high-side + low-side).

Moreover, Toshiba's Advanced Dynamic Mixed Decay (ADMD) mode, which controls current more closely than conventional mixed-decay modes, ensures smooth and quiet movement over a wider speed range by maintaining accurate step control up to high speeds.

The TB67S508FTG for 2-phase bipolar stepper-motor applications supports full-, half- and quarter-step resolution modes, and is delivered in a space-saving VQFN36 package optimised for high heat-radiation to allow superior reliability. All the usual protection features are built-in, including thermal shutdown, overcurrent detection, low-power and under-voltage detection, and terminal-component open/short-circuit detection (OSCM).

#### KEY FEATURES

**On-chip current detection, low- $R_{DS(ON)}$  output stage and advanced mixed-decay mode minimize footprint and heat generation, boost accuracy and high-speed control.**

- Capable of controlling one two-phase bipolar stepping motor
- Allows full, half(a)(b), quarter step operation
- Low on-resistance (High + Low side=0.45 Ω (typ.)) MOSFET output stage
- ACDS (Advanced Current Detect System) is applied

- It realizes current sense resistor less system
- ADMD (Advanced Dynamic Mixed Decay Mode) improves the current follow-up, achieving high-efficiency motor control even in high-speed rotation
- High voltage and current, maximum ratings 40 V/3.0 A
- Interface: selectable by pin, either CLOCK-type or PHASE-type is possible.
- Built-in error detection circuits with signal output:
  - Thermal shutdown (TSD)
  - over-current shutdown (ISD)
  - under voltage lock out (UVLO))
- VQFN36 package, only 5 × 5 mm<sup>2</sup> body size.

#### APPLICATION EXAMPLES

- Office equipment
- Surveillance cameras
- Point-of-sales, Banking Terminals
- 3D-printer
- Robots
- Banknote identification machines
- Home Appliances

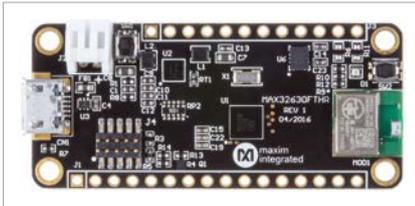


# MAX32630FTHR

## MAX32630FTHR Application Platform



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MAX32630FTHR

### MAX32630FTHR board – a rapid development platform for microcontroller MAX32630

The MAX32630FTHR board is a rapid development platform designed to help engineers to quickly implement battery optimized solutions with the MAX32630 ARM® Cortex®-M4F microcontroller. The board also includes the MAX14690 wearable PMIC to provide optimal power conversion and battery management.

The form factor is a small 0.9 × 2.0 in<sup>2</sup> dual row header footprint that is compatible with breadboards and off-the-shelf peripheral expansion boards.

Additionally, on board are a variety of peripherals including a dual-mode Bluetooth® module, micro SD card connector, 6-axis accelerometer/gyro, RGB indicator LED, and pushbutton.

This provides a power-optimized flexible platform for quick proof-of-concepts and early software development to enhance time to market.

Go to:  
<https://developer.mbed.org/platforms/MAX32630FTHR/>  
to get started developing with this board.

## KEY FEATURES

### MAX32630 Microcontroller

- ARM® Cortex®-M4F, 96 MHz
- 2048 KB Flash Memory
- 512 KB SRAM
- 8 KB Instruction Cache
- Full-Speed USB 2.0
- Three SPI Masters, One Slave
- Three I<sup>2</sup>C Masters, One Slave
- Four UARTS
- 1-Wire Master
- 66 GPIO
- 4 Input 10-Bit ADC

### MAX14690 Wearable PMIC

- Battery Charger with Smart Selector
- Dual Micro IQ Buck Regulators
- Three Micro IQ Linear Regulators
- Power-On/Off Sequencing Controller
- Voltage Monitor Multiplexer

### Expansion Connections

- Breadboard-Compatible Headers
- Micro SD Card Connector
- Battery Connector
- Micro USB Connector

### Integrated Peripherals

- RGB Indicator LED
- 6-Axis Accelerometer/Gyro
- Dual-Mode Bluetooth Module
- User Pushbutton

### mbed® HDK Debug Interface

- Drag-and-Drop Programming
- SWD Debugger
- Virtual UART Console

## APPLICATION EXAMPLES

- Fitness Monitors
- Portable Medical Devices
- Sensor Hubs
- Sports Watches
- Wearable Medical Patches



100-up price  
MKE15Z256VLH7  
**€ 2.47**

10-up price  
MKE15Z256VLL7  
**€ 2.64**

1-up price  
FRDM-TOUCH  
**€ 16.-**

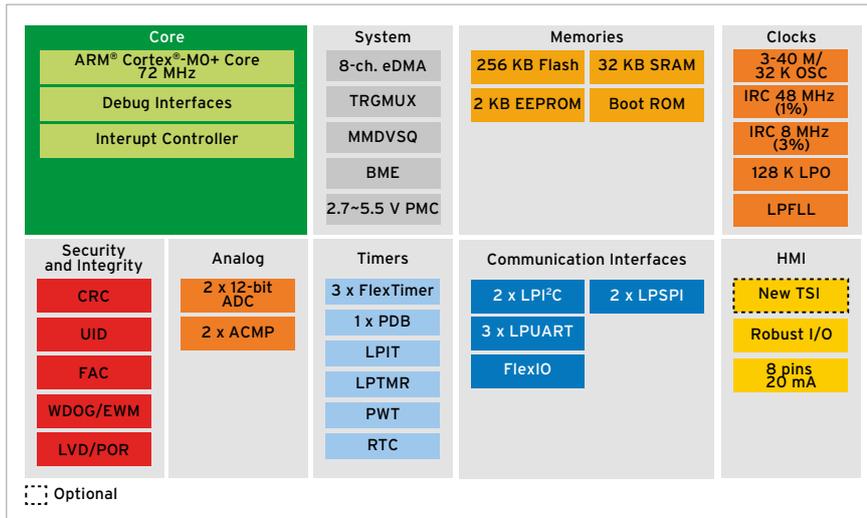
1-up price  
FRDM-KE15Z  
**€ 39.-**

## KINETIS TOUCH SOLUTION

Complete and robust touch sensing solution working simply and seamlessly on Kinetis KE15Z



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Robust MCU with 256KB Flash and TSI

The user interface of a product is a key element that design engineers need to address to provide a compelling user experience.

Touch pads, slides, and rotaries offer a more intuitive and effective way of user interaction than traditional buttons.

Designing a touch-based user interface is simplified with the NXP Touch solution. This solution includes software that is designed to work simply and seamlessly with NXP's capacitive touch sensing hardware IP that's available on Kinetis KE15Z MCUs.

### KEY FEATURES

Key benefits of the NXP Touch solution include:

- Robust EMC performance, noise immunity with IEC61000-4-6 standard certification with the 3 V and 10 V tests
- Self-cap and mutual-cap modes supporting up to 6 x 6 matrix touchpads
- Effective performance even with liquid substances such as water, salt water, oil, cold steam, etc.
- High sensitivity with a boost feature to use when the touch overlay on the user interface is thick (acrylic, glass)



FRDM-KE15Z: Freedom Development Platform

For easy starting and designing, NXP does lots of work in SW enablement – NXP Touch Library, which is based on the MCUXpresso SDK:

- APIs for high-level touch applications
- Typical design examples
- Advanced filtering and integrating detection (AFID)/Signal Adaptive Filtering Algorithm (SAFA) key detectors, robustness guarantee
- Automatic touch IP registers configuration, robustness guarantee
- GUI visibility, make debug and test easy

### APPLICATION EXAMPLES

#### Consumer

- Cooking Products
- Dish Washer
- Refrigerator
- Room Air Conditioning System

#### Health

- Activity and Wellness Monitor
- Continuous Glucose Monitor

#### Home and Building Control

- Smart Lighting

#### Infrastructure and Industry

- Thermostat



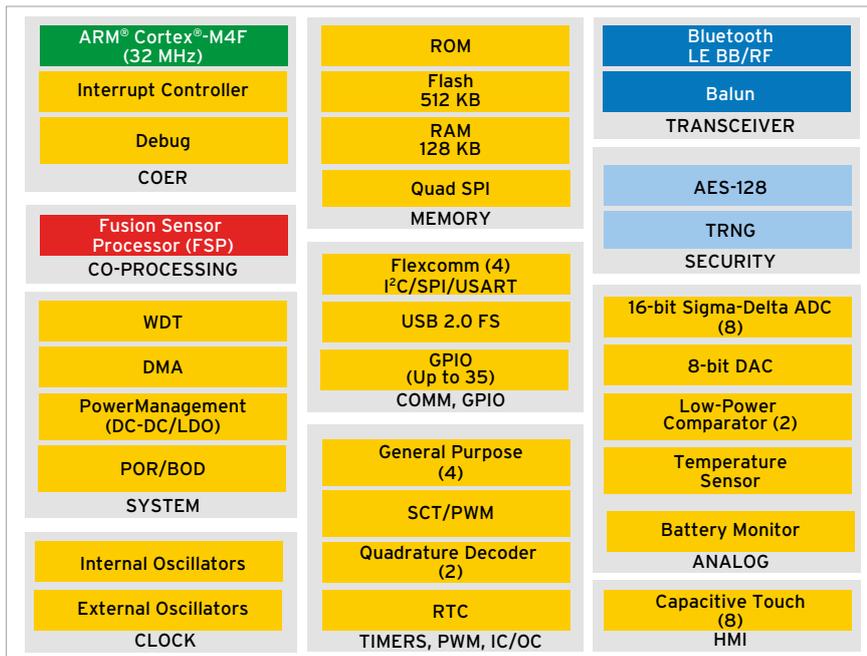
FRDM-Touch with FRDM-KE15Z

## QN908X MCU FAMILIES

Based on ARM® Cortex®-M4 core,  
Low-power, Bluetooth® 5 compliant MCUs  
with high sensitivity



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QN908x Block Diagram

**QN908x is an ultra-low power, high performance and highly integrated Bluetooth® 5 Compliant Low Energy solution for Bluetooth Smart applications**

The QN908x MCUs perform well in areas such as sports and fitness, human interface devices, and app-enabled smart accessories. The MCU family is specially designed for wearable electronics with a small capacity battery.

The QN908x devices integrate a Bluetooth® Low Energy radio, controller, protocol stack and profile software on a single chip, providing a flexible and easy to use Bluetooth® Low Energy SoC solution.

### KEY FEATURES

#### Power efficient

- 32 MHz (max) ARM® Cortex®-M4F
- Fusion Signal co-Processor (FSP) hardware acceleration for sensor fusion
- 3.5 mA Rx current @ DC-DC, 3 V
- 3.5 mA Tx current with 0 dBm Tx power @ DC-DC, 3 V

#### Large and expandable embedded memory

- 512 kB embedded program flash and 128 kB SRAM
- Quad-SPI interface to external flash for XIP

### Complete Bluetooth Low Energy protocol stack and application profiles

- Bluetooth® Low Energy v4.2 full feature support
- LE 2M PHY with Bluetooth® 5 compatibility

### High Integration

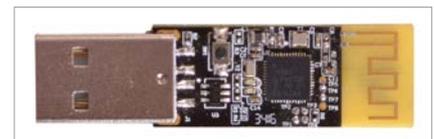
- 2 × I<sup>2</sup>C, 2 × SPI, 2 × USART
- USB 2.0 FS device
- 8-channel 16-bit high-resolution ADC
- Up to 8 touch buttons

### Reliable and secure

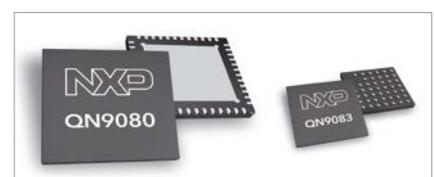
- True Random Number Generator
- Cyclic Redundancy Check
- AES-128

### APPLICATION EXAMPLES

- Wearables
- Health devices
- Sports and fitness trackers
- HID devices such as keyboards, mice, and remote control units
- Smart home nodes
- Building and home automation
- Retail and advertising beacons



QN9080DK dongle



QN908x Family Packages

# MC9S08SUX

High-voltage and high-integration 8-bit S08 family member for low-end motor-control



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MC9S08SUX

**The MC9S08SUX is a low-cost, ultra-high-voltage processor family, offering a single-chip solution for low-end 4.5...18 V motor control applications with low BOM cost and high integration.**

Based on the HSC08 core, the MC9S08SUX family is tightly integrated into the 4 × 4 mm<sup>2</sup> QFN package, which enables easy layout for space-constrained applications. Compared against discrete device solutions for motor control, using the S08SU family can save up to 50 % PCB size and layer.

Additionally, NXP has integrated virtually all of the necessary features in BLDC motor control, including zero crossing point detection, pulse width measurement, over voltage protection and over current protection, enabling developers to simply configuring registers and easily use the functions in applications.

The MC9S08SUX family also includes amplifiers for current measurement and supports three high-side PMOSes as well as three low-side NMOSes. Full software support for the MC9S08SUX MCU family is provided by CodeWarrior and IAR.

## KEY FEATURES

### Single Voltage Supply Up to 18 V

- 40 MHz HCS08 core frequency with 4.5...18 V operation voltage

### High integration

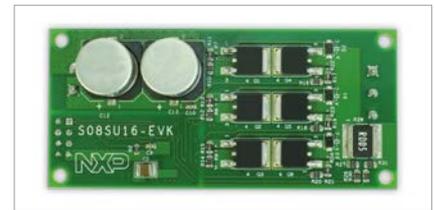
- Up to 16 KB non-volatile flash, 768 bytes internal system RAM
- 2 × 12-bit ADC with up to 10 channels
- 1 × ACMP with up to 4 inputs and internal 6-bit DAC
- High Voltage Gate Driver Unit which includes 3 × high-side PMOS pre-driver, 3 × low side NMOS pre-driver, 2 × current sensing Op-Amp & OCP, V<sub>SUP</sub> OVP
- 1 × 16-bit FlexTimer (FTM)
- 1 × 16-bit Modulo Timer (MTIM)
- 2 × 16-bit Pulse Width Timers (PWT)
- 1 × 16-bit 6-channel PWM (MCPWM)
- 1 × SCI, 1 × IIC

### Cost effective and Small Footprint

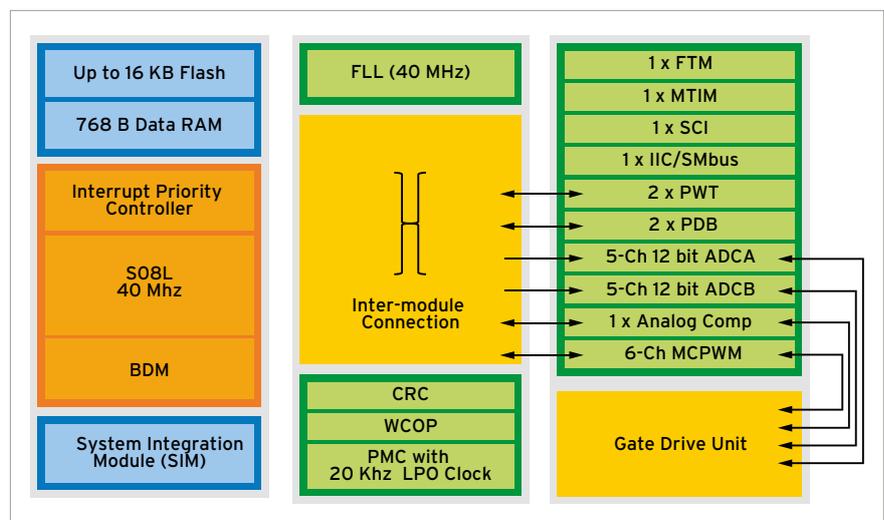
- Single chip solution offers low BOM cost.
- 4 × 4 mm<sup>2</sup> 24-pin QFN is an ideal fit for space-constrained applications

## APPLICATION EXAMPLES

- Drone ESC
- BLDC power tool
- BLDC step motor
- Cordless power tool
- Healthcare
- Robots



Sensor-less BLDC Control Reference Design: S08SU16-EVK



MC9S08SUX Block Diagram

# S12ZVMA

## Mixed-Signal MCU for Automotive & Industrial Motor Control Applications



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**The S12ZVMA MCU family is a programmable single chip solution for simple loads needed to be controlled in the car remotely via LIN or PWM command.**

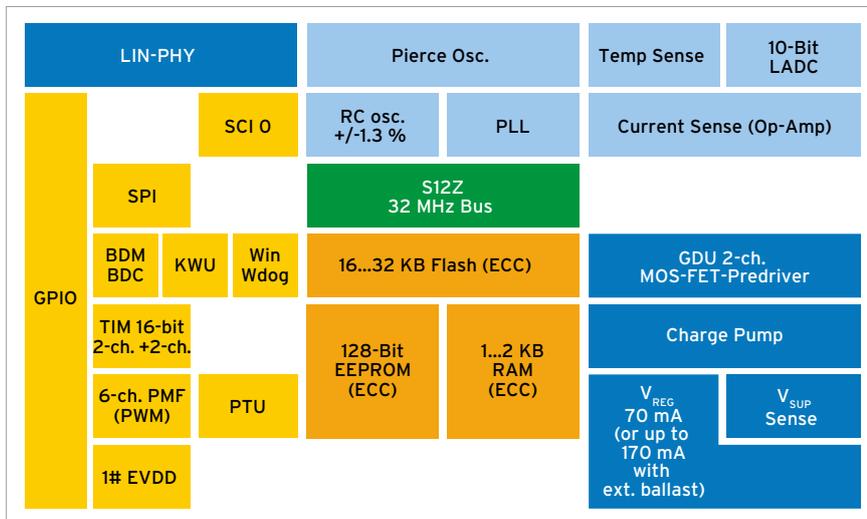
Integrated LIN-PHY, 12 V-V<sub>REG</sub> and half bridge gate driver allow building extremely compact solutions for DC-motors, solenoids or resistive loads. The S12ZVM family gives one of the smallest, most efficient and scalable

3-phase motor control solutions for industrial and automotive applications (BLDC, PMSM or SR-motor).

It integrates a sophisticated S12Z 16bit MCU together with a 12...5 V voltage regulator, LIN physical layer or CAN physical layer and gate driver unit (GDU) in order to control six power MOSFETs for automotive and industrial applications such as blowers, fans or pumps for fuel, oil or water.

### KEY FEATURES

- S12Z CPU @ 32 MHz bus speed
- V<sub>REG</sub> +5 V/70 mA; nominal 6...18 V; extended (with degradation) down to 3.5 V with boost option (fuelpump)
- HV-PHY/LIN PHY, LIN2.1/2.2/J2602 compliant
- HV-PHY allows control directly through 12 V PWM command (with error/tack response)
- 16/32 kB Flash, 2 kB RAM, 128 B EEPROM
- Half-Bridge Gate Pre-Driver for 2-NMOS control (1 × LS, 1 × HS; Gate Charge 50...80 nC)
- Integrated Bootstrap Diode (to charge the external bootstrap capacitor)



S12ZVMA Block Diagram

### APPLICATION EXAMPLES

- DC-motors (unidirectional) such as pumps, fans
- Electromechanic actuators (eg. Valves)
- Any kind of automotive load that remotely needs to get switched by a FET (via a LIN or PWM-signal) like horn, aux. heating, lighting, smart junction box, ...

# STM32L496

## Graphics with Power Efficiency



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STM32L496 Discovery Kit

**The STM32L496xx devices are ultra-low-power microcontrollers based on the high-performance ARM® Cortex®-M4 32-bit RISC core operating at a frequency of up to 80 MHz.**

The Cortex-M4 core features a Floating point unit (FPU) single precision which supports all ARM single-precision data-processing instructions and data types. It has a full set of DSP instructions and a memory protection unit (MPU) which enhances security. The STM32L496xx embed high-speed memories (up to 1 Mbyte of Flash memory, 320 Kbyte of SRAM), a flexible external memory controller (FSMC) for static memories, and a Quad SPI flash memories interface.

### KEY FEATURES

For more detailed descriptions please visit the ST home page, for example <http://www.st.com/en/microcontrollers/stm32l496ae.html>

- Core: ARM® 32-bit Cortex®-M4 CPU with FPU,
- Adaptive real-time accelerator (ART Accelerator™) allowing 0-waitstate execution from Flash memory,

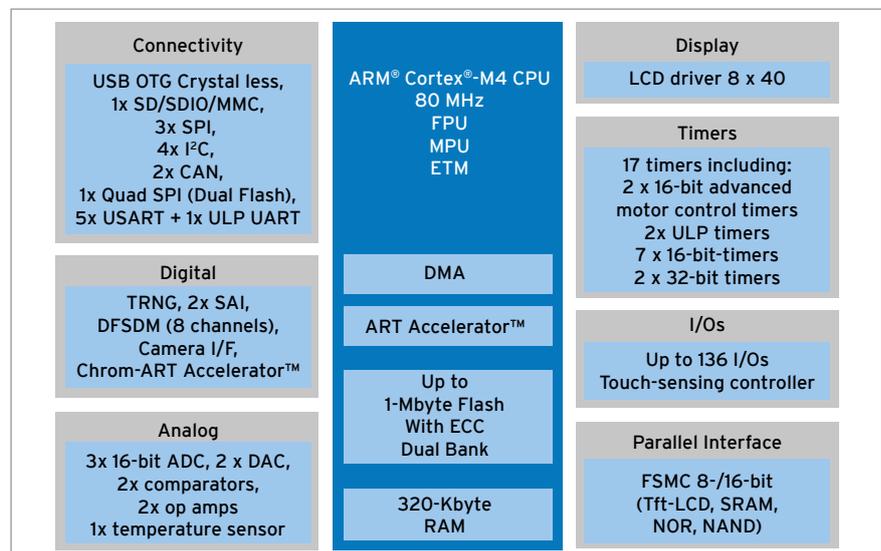
frequency up to 80 MHz, MPU, and DSP instructions

- Ultra-low-power with FlexPowerControl
- Benchmarks:
  - Performance benchmark: 100 DMIPS/1.25 DMIPS/MHz (Drystone 2.1)
  - Energy benchmark: 217 ULPbench™ score
- 16 different timers
- RTC with HW calendar, alarms and calibration
- Up to 136 fast I/Os, most 5 V-tolerant
- Dedicated Chrom-ART Accelerator™ for enhanced graphic content creation
- 8- to 14-bit camera interface up to 32 MHz (B&W) or 10 MHz (color)

- Memories
    - Up to 1 MB Flash,
    - 2 banks read-while-write,
    - proprietary code readout protection
  - Various Clock Sources, PLLs, etc.
  - LCD 8 × 40 or 4 × 44 with step-up
  - Up to 24 capacitive sensing channels
  - 4x digital filters for sigma delta modulator
  - Rich analogue peripherals (independent supply)
  - 20 communication interfaces
  - 14-channel DMA controller
  - True Random Number generator
  - CRC calculation unit, 96-bit unique ID
- Development support: serial wire debug (SWD), JTAG, Embedded Trace Macrocell™

### APPLICATION EXAMPLES

- Every wearable with graphics display
- Embedded applications requiring a lot of RAM



# ACNT-H87B/H87A/H870

## Precision Optically Isolated Voltage Sensor in a 15-mm Stretched SO-8 Package



CLICK OR SCAN



ACNT-H87B/H87A/H870

**The ACNT-H87B/H87A/H870 voltage sensors are optical isolation amplifiers designed specifically for voltage sensing.**

Its 2-V input range and high 1-GΩ input impedance, make it well suited for isolated voltage sensing requirements in electronic power converters applications including motor drives and renewable energy systems. In a typical voltage sensing implementation, a resistive voltage divider is used to scale the DC-link voltage to suit the input range of the voltage sensor.

A differential output voltage that is proportional to the input voltage is created on the other side of the optical isolation barrier. For general applications, the ACNT-H87A (±1 % gain tolerance)

and the ACNT-H870 (±3 % gain tolerance) are recommended. For high precision requirements, the ACNT-H87B (±0.5 % gain tolerance) can be used.

The ACNT-H87B/H87A/H870 family operates from a single 5 V supply and provides excellent linearity. An active-high shutdown pin is available, which reduces the  $I_{DD1}$  current to only 15 μA, making the devices suitable for battery-powered and other power-sensitive applications. The high common-mode transient immunity (15 kV/μs) of the ACNT-H87B/H87A/H870 provides the precision and stability needed to accurately monitor DC-link voltage in high noise environments.

Combined with superior optical coupling technology, the ACNT-H87B/H87A/H870 implement sigma-delta (Σ-Δ) modulation, chopper stabilized amplifiers, and differential outputs to provide unequalled isolation-mode noise rejection, low offset, high gain accuracy, and stability. This performance is delivered in a compact, auto-insertable 15-mm Stretched SO-8 (SSO-8) package that meets worldwide regulatory safety standards.

### KEY FEATURES

- Advanced sigma-delta (Σ-Δ) modulation technology
- Unity gain 1 V/V, ±0.5 % high gain accuracy (ACNT-H87B)
- 1-GΩ input impedance
- 0...2 V nominal input range
- 35 ppm/°C low gain drift
- 21 μV/°C offset voltage drift
- 0.1 % non-linearity maximum
- Active-high shutdown pin
- 100-kHz wide bandwidth
- 3...5.5-V wide supply range for output side
- -40...+110 °C operating temperature range
- 15 kV/μs common-mode transient immunity
- Compact, auto-insertable 15-mm Stretched SO-8 package
- Safety and regulatory approvals:
  - IEC/EN/DIN EN 60747-5-5: 2262  $V_{PEAK}$  working insulation voltage
  - UL 1577: 7500  $V_{RMS}$ /1 min double protection rating
  - CSA: Component Acceptance Notice #5

### APPLICATION EXAMPLES

- Isolated voltage sensing in AC and servo motor drives
- Isolated DC-bus voltage sensing in solar inverters, wind turbine inverters
- Isolated sensor interfaces
- Signal isolation in data acquisition systems
- General-purpose voltage isolation

# ACNT-H511-000E

## Open Collector Output Optocoupler in 15 mm Stretched SO8 Package



CLICK OR SCAN



ACNT-H511-000E

**The ACNT-H511 is well suited for isolated communication logic interfaces and control in high-voltage power systems such as 1500 V PV systems, 690 V<sub>AC</sub> drives, renewable inverters and medical equipment.**

ACNT-H511 is a single-channel open collector optocoupler device in 15 mm stretched SO8 package. The device is equipped with an insulating layer between the light emitting diode and

integrated photon detector to provide electrical insulation between the input and output. Having separate connections for the photodiode bias and output transistor collector reduces the base-collector capacitance and enhances the data speed up to a hundred times compared to a conventional phototransistor coupler.

### KEY FEATURES

- TTL Compatible
- Open-Collector output
- Guaranteed AC and DC performance over wide temperature: -40...+105° C
- Wide operating V<sub>CC</sub> range: 4.5...24 V
- 15mm stretched SO8 package
- Internal clearance (DTI): 0.5 mm
- 40 kV/μs typical common mode rejection (CMR) at V<sub>CM</sub> = 1500 V
- Safety and regulatory approvals

- UL 1577 Recognized: 7,500 V<sub>RMS</sub> for 1 minute
- CSA Approved
- IEC/EN 60747-5-5 Approved for Reinforced Insulation: V<sub>IORM</sub> = 2,262 V<sub>PEAK</sub> and V<sub>IOTM</sub> = 12,000 V<sub>PEAK</sub>

### APPLICATION EXAMPLES

- High voltage power systems, for example, 690 V<sub>AC</sub> drives
- Renewable energy inverters
- Feedback elements in switching power supplies
- Digital isolation for A/D, D/A conversion digital field
- Communications interface
- MCU interface

# AFBR-57B4APZ

## DC-50 MBaud 850 nm Multimode LC Duplex SFP Transceiver



CLICK OR SCAN



AFBR-57B4APZ

### DC-50 MBaud data transmission over 2 km with (62.5 µm/125 µm) multimode fibers

The SFP transceiver provides system designers the ability to implement DC-50 MBaud data transmission over 2 km with (62.5 µm/125 µm) multimode fibers. The transceiver supports LC duplex connector and is lead free, and RoHS compliant. Using the 2-wire serial

interface defined in the SFF-8472 MSA, the AFBR-57B4APZ provides real-time information on module temperature, transmitter supply voltage, and receiver average input power.

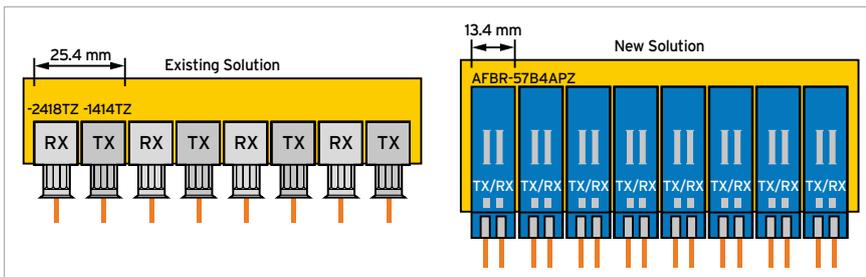
### KEY FEATURES

- Data rate support from DC to 50 MBaud
- Single 3.3 V power supply
- Manufactured in an ISO 9001 certified facility
- 850 nm VCSEL transmitter
- Link Distance up to 2 km with 62.5 µm/125 µm multimode fiber
- Low current and low power dissipation

- Hot pluggable SFP connector
- Compatible with SFP MSA specification
- Class 1 FDA IEC60825-1 laser safety compliant
- Operating temperature -40...+85 °C
- Excellent EMI and EMC behavior
- Integrated 850 nm VCSEL and driver IC with LVTTTL input logic transmitter
- Integrated PIN diode and digitalizing IC with LVTTTL output logic receiver

### APPLICATION EXAMPLES

- Power substation automation
- HVDC
- Industrial networking over multimode fiber



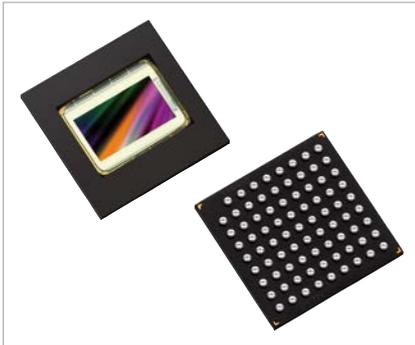
New AFBR-57B4APZ Solution doubles fiber density per board

# AR0230AT

## 2 MP 1/3" CMOS Image Sensor



CLICK OR SCAN



AR0230AT

**The AR0230AT is a 1/2.7-inch CMOS digital image sensor with an active-pixel array of 1928 H x 1088 V.**

It captures images in either linear or high dynamic range modes, with rolling-shutter readout. The device includes sophisticated camera functions such as in-pixel binning, windowing and both video and single frame modes. The AR0230AT is designed for both low light and high dynamic range scene performance.

It is programmable through a simple two-wire serial interface. This new image sensor produces extraordinarily clear, sharp digital pictures, and its ability to capture both continuous video and single frames make it the perfect choice for a wide range of applications.

### KEY FEATURES

- Outstanding low-light performance
- High Quality HD Video
- Flexible Interface Options
- High and Standard Dynamics Range Capture
- Complete Solution with Co-Processor

### APPLICATION EXAMPLES

- 1080 p Backup Camera
- 1080 p Ethernet Surround Cam

## TLX9xxx AUTOMOTIVE PHOTOCOUPLERS

### AEC-Q101 qualified photocoupler line-up



CLICK OR SCAN



Toshiba TLX9

**Toshiba Electronics Europe has introduced a range of 10 AEC Q101-qualified photocouplers designed to address the demanding isolation, interface, switching and form factor requirements of automotive applications.**

Supplied in low-profile SO6 packages, the TLX9304, TLX9378 and TLX9376 are IC output photocouplers offering respective data rates of 1 Mbps, 10 Mbps and 20 Mbps. Also in SO6 packaging are the TLX9300 and TLX9185A transistor output and TLX9905 and TLX9906 photovoltaic output devices. The TLX9000 and TLX9291A also offer transistor outputs but are supplied in half-pitch SO4 packages. An SO6-packaged photo-relay – the TLX9175J – is also available.

Toshiba's new photocouplers offer solutions that can operate at temperatures from -40...125°C (except TLX9175J:  $T_{OPR}$  -55 ~ 105°C) and suit inverter control and intelligent power module (IPM) interface applications in conventional, EV (electric vehicle) and HEV (hybrid electric vehicle) applications. The photorelay is ideally suited to the needs of automotive battery control. Despite their small size, all of the devices have creepage/clearance distances of 5 mm and offer a minimum isolation voltage of 3750 V<sub>rms</sub>.

The TLX9304, TLX9378 and TLX9376 integrate a GaAlAs infrared light emitting diode and high-gain, high-speed photodetectors. An internal Faraday shield provides a common-mode transient immunity of 15 kV/μs. In the

case of the TLX9175J photo-relay, the LED technology is based on a gallium arsenide construction. A high-voltage MOSFET between the relay's output terminals meets the needs of battery control systems.

### KEY FEATURES

- Wide range of AEC Q qualified couplers with extended temp. range from -40...+125°C
- Extensive range of data rate options up to 20 Mbps
- Optical isolation with guaranteed internal galvanic isolation distance of min. 0.4 mm
- Leading edge technology for highest reliability and lowest power consumption
- Packages with clearance and creepage distances of 5 mm

### APPLICATION EXAMPLES

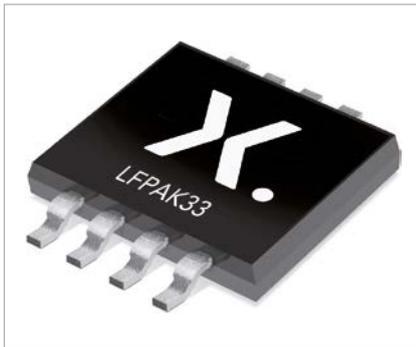
- EV (electric vehicle) and HEV (hybrid electric vehicle)
- BMSDC-DC Converter
- Inverter

# AUTOMOTIVE MOSFETS IN LFAK33

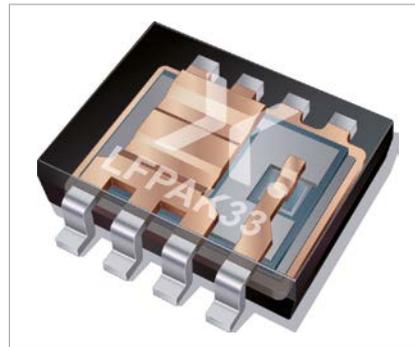
## Trench 6 Portfolio, AEC-Q101 Qualified to 175 °C –The Ultimate Automotive MOSFET



CLICK OR SCAN



LFAK33



LFAK33 package

### Bringing Nexperia’s robust and reliable copper clip technology to the Power33 (3.3 x 3.3 mm<sup>2</sup>) footprint.

The LFAK33 is the ultimate in automotive MOSFET performance within a compact power footprint and extends the LFAK family into smaller power systems. This new package uses

advanced copper clip technology and the LFAK33 portfolio can address a wide range of automotive applications. Supporting easy optical inspection and enhanced board level reliability, the LFAK33 is also footprint compatible with alternative packages in the power33 footprint.

### KEY FEATURES

- Ultra-compact footprint – 10.9 mm<sup>2</sup>
- Thermal performance – copper clip technology, no wires, no glue, 175 °C T<sub>j</sub> max.
- Electrical performance – low package resistance and inductance, up to 70 A I<sub>D</sub> max. rating
- Mechanical robustness – Exposed leads absorb thermal and mechanical stresses
- Manufacturability – Exposed leads allow for easy optical inspection

### APPLICATION EXAMPLES

#### Automotive applications:

- Braking (ABS/ESP)
- Engine management
- Transmission
- Automotive lighting
- Water pump
- Oil pump
- Fuel pump
- Electric park brake

### Portfolio extract N-Channel LFAK33 (fully automotive qualified)

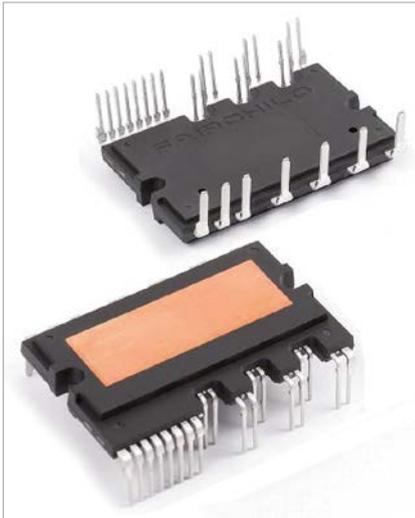
Type number	Package name	VDS [max.] (V)	R <sub>DS(ON)</sub> [max.] @ VGS = 10 V (mΩ)	ID [max.] (A)	QGD [typ.] (nC)
BUK9M156-100E	LFAK33	100	150	9.3	3.2
BUK9M34-100E	LFAK33	100	34	29	10.3
BUK7M17-80E	LFAK33	80	17	43	9.7
BUK7M27-80E	LFAK33	80	27	30	6.2
BUK9M35-80E	LFAK33	80	31	26	4.9
BUK7M19-60E	LFAK33	60	19	36	5.9
BUK7M67-60E	LFAK33	60	67	14	2.5
BUK7M9R9-60E	LFAK33	60	9.9	60	10.4
BUK7M10-40E	LFAK33	40	10	56	6.7
BUK7M45-40E	LFAK33	40	45	19	2.2
BUK7M6R3-40E	LFAK33	40	6.3	70	9.5
BUK9M17-30E	LFAK33	30	14	37	3.7
BUK9M6R6-30E	LFAK33	30	5.3	70	7.8

## SPM 3 SERIES MODULES

### 600 V & 1200 V SPM® 3 Series Modules



CLICK OR SCAN



SPM3 Package

**600 V & 1200 V SPM 3 Series Modules are an advanced Motion SPM® 3 module providing a fully-featured, high-performance inverter output stage for AC Induction, BLDC, and PMSM motors.**

These modules integrate optimized gate drive of the built-in IGBTs to minimize EMI and losses, while also providing multiple on-module protection features including under-voltage lockouts, over-current shutdown, and fault reporting. The built-in, high-speed HVIC requires only a single supply voltage and translates the incoming logic-level gate inputs to the high-voltage, high-current drive signals required to properly drive the module's internal IGBTs. Separate negative IGBT terminals are available for each phase to support the widest variety of control algorithms.

#### KEY FEATURES

- Transfer molded modules with very high power cycling capability
- Low thermal resistance with DBC Substrate
- Built-in real NTC thermistor on DBC
- Low-loss, short-circuit rated IGBTs
- Built-In bootstrap diodes and dedicated Vs pins simplify PCB layout
- Separate open-emitter pins from low-side IGBTs for three-phase current sensing

#### APPLICATION EXAMPLES

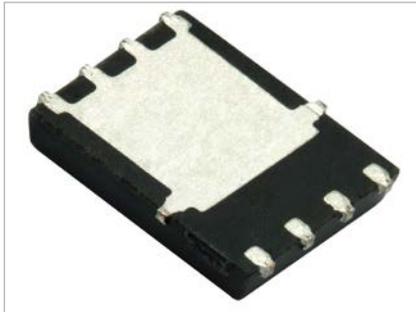
- Servo Drives
- Industrial Inverters
- Industrial Pumps
- Industrial Fans

# SIR626DP

## New High-End Class 60 V 1.7 mΩ MOSFET SiR626DP in PowerPAK® SO8



CLICK OR SCAN



Product image bottom view



Product image top view

**Vishay Intertechnology introduces a new, very high end 60 V, 1.7 mΩ MOSFET SiR626DP In PowerPAK® SO8.**

Designing in this part, you will elevate efficiency and performance instantly with minimum circuit rework.

It enables very high system efficiency, increasing both, power density and output current per device. SiR626DP is ideal for high power output designs such as synchronous rectification, 24 V systems, Motor drive control, DC/DC topologies, Solar micro inverters and more.

### KEY FEATURES

- Superior  $R_{DS(on)} \cdot C_{oss}$  FOM
- 40 % lower  $R_{DS(on)}$  than the prior generation
- Reduces I<sup>2</sup>R conduction loss
- Achieves similar  $R_{DS(on)}$  in an 80 % smaller package footprint comparing to the last generation

### Advanced Package Construction

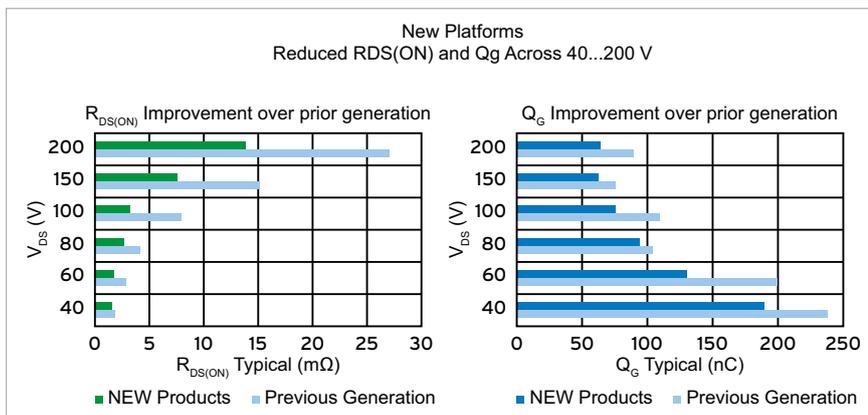
- Optimized interconnection design reduces package resistance by 66 %
- Maximize the performance of silicon and minimizes conduction loss, operating temperature

### APPLICATION EXAMPLES

- Synchronous rectification
- 24 V systems
- Motor drive control
- DC/DC topologies
- Solar micro inverters
- Power tools and industrial

### KEY BENEFITS

- New next generation technology provides very low on-resistance and ultra-low figure of merit (FOM)
- $Q_{GD}/Q_{GS}$  ratio <1 improves immunity to  $C \cdot dV/dt$  gate coupling
- Very low  $Q_{GD}$  "miller" charge reduces power loss from charging through plateau voltage
  - Reduce switching related power loss and "miller" time
- Selective products have logic level and standard gate drive capabilities
  - Can lower losses by enabling lower gate drive
  - Allows use of lower-voltage, lower cost 5 V PWM ICs
  - Low FOM helps reduce switching losses
- Wide package variety including TO packages and thermally advanced, space saving PowerPAK®
- Devices in TO packages have maximum junction temperature rated up to 175 °C



# MAX17242/3

## 3.5...36 V, 2 A/3 A, Synchronous Buck Converter with 1 mA Quiescent Current and Reduced EMI



CLICK OR SCAN

**The MAX17242/MAX17243 are high-efficiency, synchronous converters with integrated MOSFETs that help to minimize external component count and reduce total cost of solutions for a variety of applications.**

The MAX17242/MAX17243 high-efficiency, synchronous step-down DC-DC converters with integrated MOSFETs operate over a 3.5...36 V input voltage range and can operate in drop-out condition by running at 99 % duty cycle. The converters deliver up to 2 A (MAX17242) and 3 A (MAX17243) output current and generate fixed output voltages of 3.3 V/5 V, along with the ability to program the output voltage between 1...10 V.

The devices use a current-mode-control architecture and can be operated in the pulse-width modulation (PWM) or pulse-frequency modulation (PFM) control schemes. PWM operation provides constant frequency operation at all loads and is useful in applications sensitive to switching frequency. PFM operation disables negative inductor current and additionally skips pulses at light loads for high-efficiency operation. The low-resistance, on-chip MOSFETs ensure high efficiency at full load and simplify the layout.

The devices are available in a compact 20-pin (5 × 5 mm<sup>2</sup>) TQFN package with exposed pad. These parts are rated for -40...+85 °C operation.

### KEY FEATURES

#### Eliminates External Components and Reduces Total Cost

- No Schottky-Synchronous Operation for High Efficiency and Reduced Cost
- Simple external RC Compensation for Stable Operation at Any Output Voltage
- All-Ceramic Capacitor Solution: Ultra-Compact Layout with as Few as Eight External Components
- PGOOD Output and High-Voltage EN Input Simplify Power Sequencing

#### Reduces Number of DC-DC Converters to Stock

- Pin Compatibility for 2 A/3 A Options
- Fixed Output Voltage with ±2 % Accuracy (5 V/3.3 V) or Externally Resistor Adjustable (1...10 V) with ±1 % FB Accuracy
- 220 kHz...2.2 MHz Adjustable Frequency with External Synchronization

#### Reduces Power Dissipation

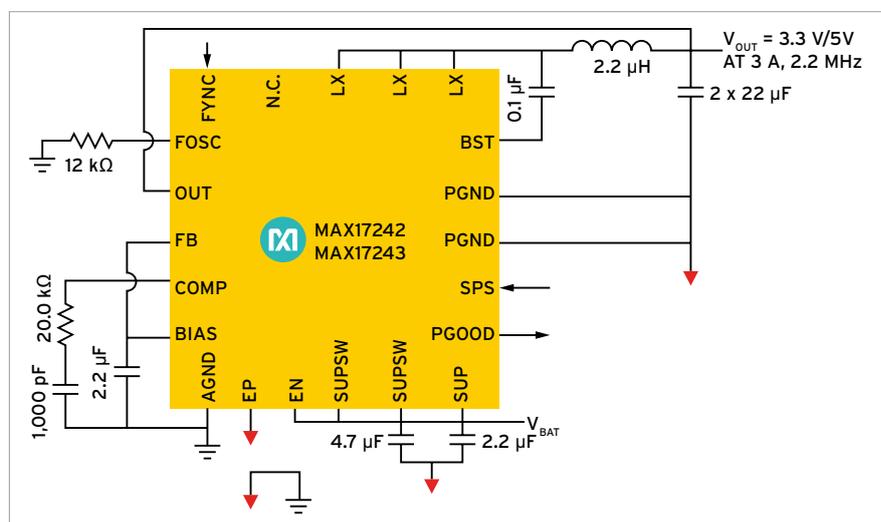
- 93 % Peak Efficiency
- Shutdown Feature Blocks Current Flow from Input-to-Output or Vice-Versa
- Less Than 5 µA (typ.) in Shutdown
- Low 15 µA (typ.) Quiescent Current in Standby Mode

#### Operates Reliably

- 42 V Input Voltage Transient Protection
- Fixed 8ms Internal Software Start Reduces Input Inrush Current
- Cycle-by-Cycle Current Limit, Thermal Shutdown with Automatic Recovery
- Reduced EMI Emission with Spread-Spectrum Control

### APPLICATION EXAMPLES

- Distributed Supply Regulation
- General-Purpose Point-of-Load
- Wall Transformer Regulation



MAX17242/MAX17243



# NCP137

## CMOS Linear Voltage Regulator (LDO) with Bias Rail, 700 mA Very Low Dropout

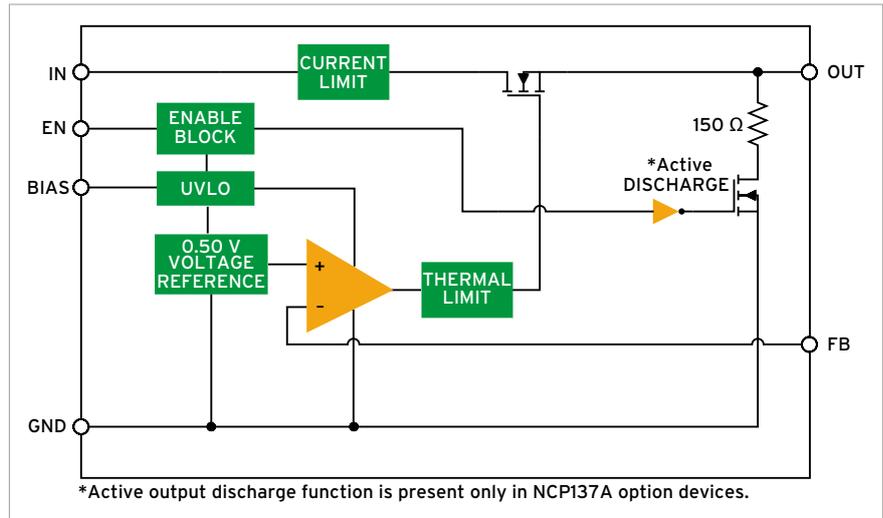


CLICK OR SCAN

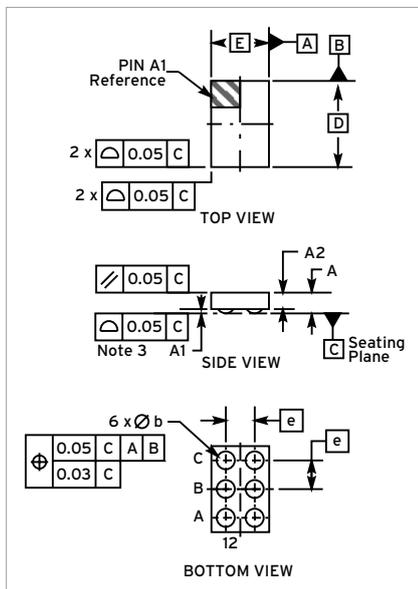
**NCP137 is a 700 mA LDO equipped with NMOS pass transistor and a separate bias supply voltage (VBIAS).**

The device provides very stable, accurate output voltage with low noise suitable for space constrained, noise sensitive applications. In order to optimize performance for battery operated portable applications, the NCP137 features low IQ consumption.

The WLCSP6 1.2 × 0.8 mm<sup>2</sup> package is optimized for use in space-constrained applications.



Block Diagram Adjustable Version



NCP137 dimensions

### KEY FEATURES

- Ultra-Low Dropout of Typ. 40 mV
- 0.5 % Typical Output Voltage Accuracy
- Guaranteed Output Current from 0...700 mA

### APPLICATION EXAMPLES

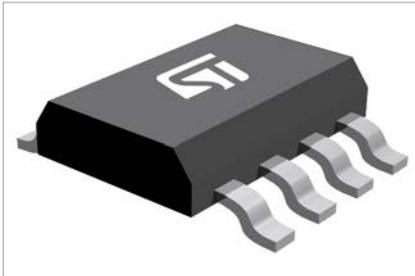
- Battery-powered and Portable Equipment
- Smartphones, Tablets
- Cameras, DVRs, Camcorders
- Technical Documentation & Design Resources

# ST1S40

## 3 A DC Step-Down Switching Regulator



CLICK OR SCAN



SO-8 Package

**The ST1S40 device is an internally compensated 850 kHz fixed-frequency PWM synchronous step-down regulator. The ST1S40 operates from 4.0...18 V input, while it regulates an output voltage as low as 0.8 V and up to  $V_{IN}$ .**

The ST1S40 integrates a 95 m $\Omega$  high side switch and 69 m $\Omega$  synchronous rectifier allowing very high efficiency with very low output voltages. The peak current mode control with internal compensation delivers a very compact solution with a minimum component count.

The device is available in HSOP-8, VFQFPN 4  $\times$  4 mm<sup>2</sup> – 8 lead, and standard SO8 package.



VFQFPN 4  $\times$  4 Package

### KEY FEATURES

- 3 A DC output current
- 4.0...18 V input voltage
- Output voltage adjustable from 0.8 V
- 850 kHz switching frequency
- Internal soft-start
- Integrated 95 m $\Omega$  and 69 m $\Omega$  Power MOSFETs
- All ceramic capacitor
- Enable
- Cycle-by-cycle current limiting
- Current fold back short-circuit protection
- Available in HSOP-8, VFQFPN4 $\times$ 4-8L, and SO8 packages

### APPLICATION EXAMPLES

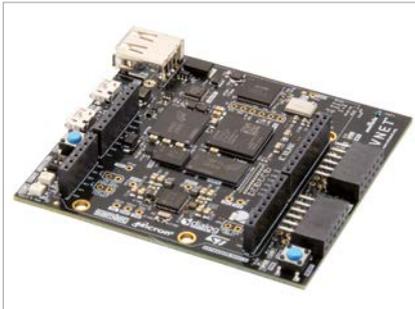
- Industrial power supplies
- Factory and building automation
- $\mu$ P/ASIC/DSP/FPGA core and I/O supplies
- smart city/home: metering, appliances, automation

# MINIZED ZYNQ DEVELOPMENT BOARD

## Xilinx programmable SOC Platform for Embedded Vision and Industrial IoT Systems



CLICK OR SCAN



MiniZed

**MiniZed represents a low cost entry point to the scalable Xilinx Zynq-7000 All Programmable SoC development platform.**

This compact design features on-board connectivity through USB, Wi-Fi and Bluetooth. Peripherals can be plugged into dual Pmod-compatible connectors, the Arduino-compatible shield interface or the USB 2.0 host interface. JTAG circuitry is incorporated onto the MiniZed base board, so with a single micro-USB cable to your laptop you are already up and running. User LED's, a button and a switch allow for a physical board interface.

Micron memory solutions are presented for QSPI flash, DDR3L memory and on-board eMMC instead of an external SD card. The Murata Type 1DX wireless solution incorporates 802.11b/g/n Wi-Fi as well as Bluetooth 4.1, which

provides both Bluetooth Classic and Low Energy (BLE). The integrated power supply generates all on-board voltages, while an auxiliary supply input can be used to power designs that require additional current. From ST Micro there is an on-board motion and temperature sensor, as well as a digital microphone.

MiniZed provides for an efficient hardware reference design, while it is also an inexpensive board that can be used to run workshops and tutorials. The board aims to showcase the power of Zynq, where the Cortex A9 processor core integrates seamlessly with the programmable fabric to provide signal processing and control solutions. The on-board digital microphone serves as an input for a variety of illustrations of how to implement FIR filters, FFT's and direct memory access.

### KEY FEATURES

- On-board connectivity through the Murata "Type 1DX" wireless module that provides
  - Wi-Fi 802.11b/g/n
  - Bluetooth 4.1 plus EDR and BLE (Bluetooth Low Energy)
- On-board memory from Micron
  - 512 MB DDR3L
  - 128 Mb QSPI flash
  - 8 GB eMMC

- Peripherals can be plugged into
  - Dual Pmod-compatible connectors (16 GPIO)
  - Arduino-compatible shield interface (22 GPIO)
  - USB 2.0 host interface
- Physical interface
  - Two bi-element user LED's
  - User pushbutton
  - Reset pushbutton
  - User switch
- The integrated power supply from Dialog generates all on-board voltages. An auxiliary microUSB supply input can be used to power designs that require additional current.
- On-board MEMS sensors from ST Micro
  - LIS2DS12 Motion and temperature sensor
  - MP34DT05 Digital Microphone
- JTAG circuitry is incorporated onto the MiniZed base board. Only a single microUSB cable is required for power, programming, JTAG emulation, and serial terminal debug output.

### APPLICATION EXAMPLES

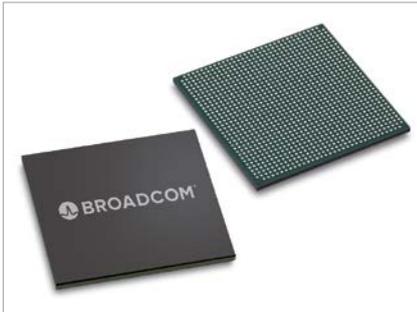
- Embedded Vision
- Industrial IoT Systems

## BCM53161

# A 2.5GE Multi-Port Switch Series with High-Speed 10GE Uplinks



CLICK OR SCAN



BCM53161

**Broadcom's BCM53161/BCM53162 is a family of 2.5GE switches designed for high-end and cost-effective multi-GE applications in the SMB, Industrial, and service provider markets.**

The BCM53161/BCM53162 switches support four 2.5GE SGMII+ ports and two 2.5GE/5GE/10GE XFI/SFI ports. BCM53162 also supports eight 10/100/1000Base-T ports with integrated GPHYs.

This switch family is based on the 28 nm RoboSwitch™ architecture, also known as Robo II. Products in this family integrate the powerful ARM® Cortex®-M7 CPU that enables customers to design switching platforms without adding an external processor. They are also equipped with high-end forwarding mechanisms that are required in many advanced applications.

The BCM53161/BCM53162 family of switches is supported by multiple software packages designed for different applications. Basic unmanaged software can be used to construct unmanaged switches with basic functionalities and no VLAN segregation.

Web-managed software, also known as Robo-OST™, can be used to build web-controlled switches, and fully-featured SDK software can be used to construct managed devices that are controlled by an external CPU, for example, home-gateways and MDUs where BCM53161/BCM53162 chips are connected to an external PON or DSL SoC.

Broadcom's BCM53161/BCM53162 chips can be used to construct standalone platforms with low port-count configuration (up to 15 ports), or to construct, using a cascaded design, a double port-count switch by connecting two chips back to back.

BCM53161/BCM53162 switches are offered to customers in commercial-grade as well as Industrial-grade temperature ambient.

**There are two different types:**

- BCM53161: 2.5 Gig-Ethernet Robo II switch with 4 x 1000FX/2500FX SGMII+ ports + 2 x 10000FX XFI/SFI ports
- BCM53162: 2.5 Gig-Ethernet Robo II switch with 8 x 10/100/1000Base-T ports with Integrated GPHYs

### KEY FEATURES

- Four 1GE/2.5GE SGMII+ ports.
- Two 2.5GE/5GE/10GE XFI/SFI ports.
- Up to Eight 10/100/1000Base-T ports with Integrated GPHYs.
- RGMII, MII, and RvMII interface to an external CPU (can also be used as a 1GE customer-facing port).

- Integrated ARM®-Cortex® M7 CPU for cost-effective applications.
- Support for cascading mode that enables the creation of a unified, single CPU, managed 16-26 port switch.
- Support high-end QoS capabilities with hierarchical scheduling, eight egress buffer queues per port each with its own shaper, WRED and Tail-drop congestion avoidance, and dual-leaky bucket ingress rate-limiters (also known as policers).
- Advanced Compact Filter Processor (CFP), also known as the Access-List engine that can classify L1-L4 headers.
- Support for Virtual Switching Instances (VSI) for explicit segregation of bridging domains with advanced VLAN translation and encapsulation (for example, Mac-in-Mac).
- Port-Extender support per 802.1BR for design of modular chassis devices or pizza box platforms with remote port extensions.
- Advanced Time Sensitive Networking (TSN) protocol support such as 802.1AS to synchronize traffic transmission across entire network domain.
- Power-saving green technology per IEEE 802.3az Energy Efficient Ethernet.

### APPLICATION EXAMPLES

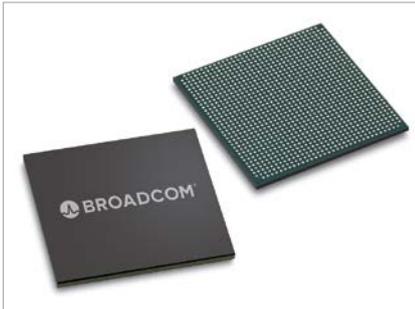
- Unmanaged/Web-Managed/Managed SMB Switch
- Home Gateway MDU
- Port Extender
- Industrial Ethernet

# BCM54210S

## Single-Port Copper/Fiber Gigabit Ethernet Transceiver



CLICK OR SCAN



BCM54210S

**The Broadcom® BCM54210S is a fully integrated Gigabit copper/fiber transceiver with support for Energy Efficient Ethernet (EEE), Synchronous Ethernet (SyncE), and IEEE 1588v2 standards.**

The PHY performs all of the physical layer functions for 10BASE-T, 100BASE-FX, 100BASE-TX, 1000BASE-T, 1000BASE-X, SGMII-Slave, and on standard Category 5 unshielded twisted-pair (UTP) cable. The BCM54210S supports both the SGMII and RGMII industry standards. The BCM54210S is based on Broadcom's proven digital-signal processor technology, combining digital adaptive equalizers, ADCs, phase-locked loops (PLL), line drivers, encoders, decoders, echo cancelers, crosstalk cancelers, and all other required support circuitry integrated into a single, monolithic CMOS chip. Designed for reliable operation over worst-case Category 5 cable plants, the BCM54210S automatically negotiates with any transceiver on the opposite end of the wire to agree on an operating

speed. The PHY can also evaluate the condition of the twisted-pair wiring to ensure that the wiring can support operation at gigabit speeds, as well as detect and correct most common wiring problems. The device continually monitors both the wiring and the opposing transceiver and alerts the system if it detects potential problems with reliable operation.

### KEY FEATURES

- SGMII or RGMII interfaces
- RGMII: 1.8 V HSTL, 2.5 V CMOS, or 3.3 V CMOS
- Supports two power supplies (3.3 V and 1.0 V) or one 3.3 V power supply using an internal voltage regulator
- Supports IEEE 802.3-compliant copper-line interfaces:
  - 1000BASE-T
  - 100BASE-TX
  - 10BASE-T
- Supports the following fiber line interfaces:
  - 1000BASE-X
  - 100BASE-FX
  - SGMII-slave
- Support for Media Converter mode
- IEEE 802.3az compliant (EEE)
  - Supports native EEE MACs
  - Supports legacy non-EEE MACs using AutogrEEEn® mode
- IEEE 1588v2 compliant
  - One-step or two-step clock
  - On-chip timestamping

- ITU-T Y.1731 delay measurement support
  - On-chip timestamping
  - One-way and two-way in both directions
- SyncE support
  - Recovered clock and clock lock outputs
  - SyncE+
- IEEE 802.3bf latency data
- Supports jumbo packets up to 18 KB
- Low-cost 25 MHz crystal option
- Ethernet@Wirespeed™
- Cable plant diagnostics that detect cable plant impairments
- Wake-on-LAN (WOL)
- Voltage and temperature monitors
- Programmable LEDs
- Robust Cable ESD (CESD) tolerance
- Low EMI emissions
- IEEE 1149.1 and 1149.6 (AC-JTAG) boundary scan
- Package: 64-pin QFN

### APPLICATION EXAMPLES

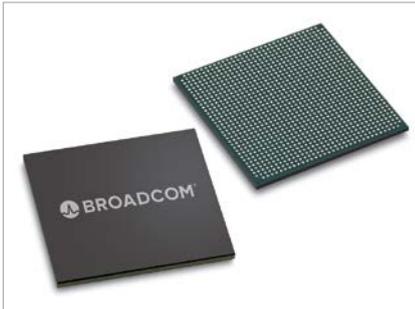
- Ethernet switches and routers for Enterprise and Carrier-Class Networking
- Small and medium business (SMB)
- Industrial automation
- Smart grid markets

## BCM89811

### BroadR-Reach® Single-Port Automotive Ethernet Transceiver



CLICK OR SCAN



BCM89811

**Integrated internal regulators provide on-chip power and eliminate the need for external regulators.**

BCM89811 is Broadcom's next generation BroadR-Reach® automotive 100Base-T1 compliant Ethernet chip, optimized for use in low-power automotive applications.

Delivering 100 megabit-per-second (Mb/s) performance over a single, unshielded twisted-pair wire, the highly integrated chip combines the functionality of multiple discrete devices in an ultra-small package ( $6 \times 6 \text{ mm}^2$ ) and meets stringent automotive EMC and ESD requirements.

#### KEY FEATURES

- Automotive-qualified low-power design reduces power consumption by up to 30 percent compared to previous generation
- On-chip integrated low-pass filters emissions (to meet automotive EMC requirements)
- Integrated internal regulators provide on-chip power and eliminate the need for external regulators
- Exceeds automotive specifications for noise cancellation and transmission jitter
- Delivers 100 Mb/s over single, unshielded twisted-pair wiring, delivering up to an 80 percent reduction in connectivity cost and 30 percent reduction in cabling weight

#### APPLICATION EXAMPLES

- Automotive

# F2972 & F2976, SPDTR, RF SWITCH

## Reflective RF Switches with Wide Frequency Range, Low Distortion, and Low Insertion Loss



CLICK OR SCAN



F2972 & F2976

**F2972 and F2976 are the first in a family of single pole double throw reflective RF switches in small 2 x 2 mm<sup>2</sup> packages.**

These devices have a wide frequency range of 5 MHz...10 GHz, and offer an excellent combination of low insertion loss, low distortion, and high isolation. Due to the reflective termination on the unused RF port, these devices have been specified for the use in either 50 or 75 Ω systems making them ideal for a wide range of markets and applications.

The F2972 and F2976 have similar RF performance and pin out with the exception of a unique feature pin. F2972 has an enable/disable pin for green, low power consumption applications. F2976 has a logic select feature pin that allows the user to invert the switch control logic enabling the control of multiple switches with a single control line that need to be in different states.



F2972



F2976

### KEY FEATURES

- Low insertion loss of 0.36 dB and isolation of 42 dB at 2000 MHz
- Extremely low distortion allows these devices to be used in a wide range of applications
  - IIP2/IIP3: 120/77 dBm
  - H2/H3: -100/-110 dBc
  - CSO/CTB: >100/>100 dBc
- Datasheet supports both 50 Ω and 75 Ω specifications
  - F2972 has Enable/Dis-able (EN) feature to conserve power consumption
  - F2976 has Logic Select (LS) feature for maximum control logic flexibility
- Silicon based semiconductor technology
- Wide 3.3/5 V V<sub>DD</sub> and 1.8/3.3 V control voltage ranges
- Wide -40...105 °C temperature range with excellent thermal characteristics

### APPLICATION EXAMPLES

- 3G/4G Small cells
- DOCSIS 3.1 CATV applications; Headend, HFC, Set-top box
- Fiber Infrastructure
- Drones
- Communication Equipment
- Harmonic filter switching
- General purpose consumer applications

## ST25R3911B

### NFC/HF RFID Reader IC



CLICK OR SCAN



ST25R3911B

**The ST25R3911B is a highly integrated NFC Initiator/HF Reader IC, including the analog front end (AFE) and a highly integrated data framing system for ISO 18092 (NFCIP-1) initiator, ISO 18092 (NFCIP-1) active target.**

ISO 18092 (NFCIP-1) active target, ISO 14443A and B reader (including high bit rates) and FeliCa™ reader. Implementation of other standard and custom protocols like MIFARE™ Classic is possible using the AFE and implementing framing in the external microcontroller (Stream and Transparent modes).

The ST25R3911B is positioned perfectly for the infrastructure side of the NFC system, where users need optimal RF performance and flexibility combined with low power.

Thanks to automatic antenna tuning (AAT) technology, the device is optimized for applications with directly driven antennas. The ST25R3911B is alone in the domain of HF reader

ICs as it contains two differential low impedance ( $1 \Omega$ ) antenna drivers.

The ST25R3911B includes several features that make it well suited for low power applications. It contains a low power capacitive sensor that can be used to detect the presence of a card without switching on the reader field. The presence of a card can also be detected by performing a measurement of amplitude or phase of signal on antenna LC tank, and comparing it to the stored reference. It also contains a low power RC oscillator and wake-up timer that can be used to wake up the system after a defined time period, and to check for the presence of a tag using one or more low power detection techniques (capacitive, phase or amplitude).

The ST25R3911B is designed to operate from a wide (2.4...5.5 V) power supply range; peripheral interface IO pins support power supply range from 1.65...5.5 V.

#### KEY FEATURES

- ISO 18092 (NFCIP-1) Active P2P
- ISO 14443A, ISO14443B and FeliCa™
- Supports VHBR (3.4 Mbit/s PICC to PCD framing, 6.8 Mbit/s AFE and PCD to PICC framing)
- Capacitive sensing – Wake-up
- Automatic antenna tuning system providing tuning of antenna LC tank
- Automatic modulation index adjustment
- AM and PM (I/Q) demodulator

- channels with automatic selection
- DPO (Dynamic Power Output)
- Up to 1.4 W in case of differential output
- User selectable and automatic gain control
- Transparent and Stream modes to implement MIFARE™ Classic compliant or other custom protocols
- Possibility of driving two antennas in single ended mode
- Oscillator input capable of operating with 13.56 MHz or 27.12 MHz crystal with fast start-up
- 6 Mbit/s SPI with 96 bytes FIFO
- Wide supply voltage range from 2.4...5.5 V
- Wide temperature range: -40...125° C
- QFN32, 5 x 5 mm<sup>2</sup> package

#### APPLICATION EXAMPLES

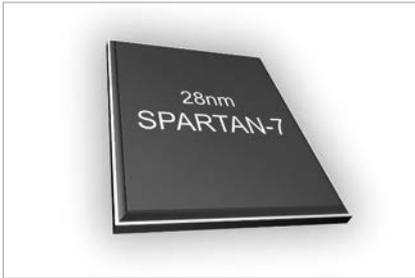
- Parameter setting
- Device maintenance
- Barcode replacement
- IoT devices

# SPARTAN-7 FPGAS

## I/O Optimization with High Performance-Per-Watt



CLICK OR SCAN



**Based on the efficient 7 series CLB architecture, enhanced DSP, and block RAM, Spartan®-7 devices offer roughly a 50 % power reduction vs. previous Spartan families, while at the same time delivering a 30 % performance improvement.**

Spartan-7 FPGAs offer a small form factor connectivity solution with high performance-per-watt. Manufactured with TSMC's 28 nm HPL process, the Spartan-7 FPGA family brings together capabilities of the Xilinx 28 nm FPGA architecture with small form factor and RoHS-compliant packaging for an optimized connectivity solution.

Spartan-7 devices enable key connectivity and processing applications in industrial, automotive, infotainment, consumer, and communications markets, among others. They are supported by the free Vivado® HL WebPACK™ Edition to help increase design productivity. Vivado IP Integrator provides correct-by-construction block-level design with a catalog of over 200 available IP solutions, while proven place and route technology enables faster timing closure and higher utilization.

### KEY FEATURES

#### 28 nm TSMC HPL Process Technology

- Scalable 7 series CLB architecture
- Look-up tables are configurable as logic, distributed RAM, or shift registers
- From 6...102 K logic cells for system-level integration

#### Cost-Optimized

- Multiple efficient integrated blocks, including XADC dual 12-bit analog-to-digital converters with supply voltage and thermal monitoring
- Optimized selection of I/O standards

#### Embedded Processing

- MicroBlaze™ soft processor with 32-bit MicroBlaze soft processor capable of running over 200 DMIPs with 3 presets including: micro-controller, real-time and application processors

#### Integrated Memory Block Capacity up to 4.2 Mb

- Efficient and high-performance block RAM with byte write enables optional FIFO configuration. Split 36 k to 2 × 18 k

#### Soft Memory Controller

- DDR3/DDR3L/DDR2/LPDDR2 support
- Data rates up to 800 Mb/s (25.6 Gb/s peak bandwidth)
- Ultimate pinout flexibility
- Software wizard

#### SelectIO™ Interface Technology

- Up to 1.25 Gb/s LVDS data rate, with up to 240 Gb/s aggregate bandwidth

- 3.3...1.2 V I/O standards and protocols
- HSTL/SSTL memory interfaces
- Adjustable slew rates

#### Efficient DSP48E1 Slices

- Fast 18 × 25 wide multiplier with 48-bit accumulator and 25-bit pre-add per slice
- Up to 176 GMACs at 551 MHz
- Pipelining, balancing, cascading, SIMD support, integrated pattern detect and ALU

#### Design Security

- Device DNA serial number and eFUSE identifier
- AES256 decryption and SHA-256 authentication for bitstream
- Tamper monitoring and response

#### Small, RoHS 6/6 Compliant Packaging

- 8...27 mm package footprints
- Extensive footprint-compatible package migration

### APPLICATION EXAMPLES

#### Machine Vision Interfacing

- Support of multiple sensors and connectivity options
- Programmable system integration
- MicroBlaze soft processor as host

#### Single-Axis Motor Control

- Supporting multiple communication standards
- Single-chip design for high reliability
- MicroBlaze soft processor for system control and comm.
- Full suite of production ready IP for fast design productivity

## LC717A30UJ

# Capacitance-Digital-Converter for Electrostatic Capacitive Touch Sensors



CLICK OR SCAN



LC717A30UJ

**The LC717A30UJ is a high performance and cost-effective capacitance converter for electrostatic capacitive touch and proximity sensors.**

The device offers 8 capacitance-sensing input channels, ideal for use in systems that require an array of switches. The LC717A30UJ reduces design time via an automatic calibration function and minimal external components. The detection result (ON/OFF) for each sensor is read out by the serial interface (I<sup>2</sup>C or SPI).

### KEY FEATURES

- Detection system: Differential capacitive detection using mutual capacitance
- Sensor input pads: Operates with small to large capacitance sensor input pads
- Input capacitance resolution: Capacitance detection down to femto-Farad level
- Measurement time 16 ms for 8 sensors
- Minimal external components
- Selectable interface : I<sup>2</sup>C or SPI
- Current consumption : 0.8 mA ( $V_{DD} = 5.5 V$ )
- Supply voltage : 2.6...5.5 V
- AEC-Q100 qualified and PPAP capable

### APPLICATION EXAMPLES

- Automotive
- Consumer
- Industrial
- Computing
- Lighting

# LIS2DW12

## High-Performance Ultra-Low-Power 3-Axis Acceleration Sensor



CLICK OR SCAN



LIS2DW12

**The LIS2DW12 is an ultra-low-power high-performance three-axis linear accelerometer belonging to the “femto” family which leverages on the robust and mature manufacturing processes already used for the production of micromachined accelerometers.**

The LIS2DW12 has user-selectable full scales of  $\pm 2$  g/ $\pm 4$  g/ $\pm 8$  g/ $\pm 16$  g and is capable of measuring accelerations with output data rates from 1.6...1600 Hz. The device has an integrated 32-level first-in, first-out (FIFO) buffer allowing the user to store data in order to limit intervention by the host processor.

The embedded self-test capability allows the user to check the functioning of the sensor in the final application.

The LIS2DW12 has a dedicated internal engine to process motion and acceleration detection including free-fall, wakeup, highly configurable single/double-tap recognition, activity/inactivity, portrait/landscape detection and 6D/4D orientation.



LIS2DW12 in application

The sensor is available in a small thin plastic land grid array package (LGA) and it is guaranteed to operate over an extended temperature range from  $-40...+85^{\circ}$  C.

### KEY FEATURES

- Ultra-low power consumption: 50 nA in power-down mode
- Down to 380 nA in active low-power mode
- Very low noise: down to 1.3 mg RMS in low-power mode
- Multiple operating modes with multiple bandwidths
- Supply voltage, 1.62...3.6 V
- Independent IO supply
- $\pm 2$  g/ $\pm 4$  g/ $\pm 8$  g/ $\pm 16$  g full scale
- High-speed I<sup>2</sup>C/SPI digital output interface

- Single data conversion on demand
- 14-bit resolution
- Embedded temperature sensor
- Self-test
- 32-level FIFO
- 10000 g high shock survivability

### APPLICATION EXAMPLES

- Motion detection for wearables
- Gesture recognition and gaming
- Motion-activated functions and user interfaces
- Display orientation
- Tap/double-tap recognition
- Free-fall detection
- Smart power saving for handheld devices

# HERACLES

## A Complete Quad-Band Ready to Use 2G Module with Embedded SIM Card and Prepaid Data Plan



CLICK OR SCAN



The Heracles Module

**Heracles is a complete Quad-band GSM/GPRS in an LGA type with embedded M2M GSM card. It transmits voice, SMS and data information with low-power consumption. With tiny size of 15,8 x 17,8 x 2,4 mm<sup>3</sup>, it can fit into slim and compact customer designs.**

Designed to simplify the IoT device making process, Heracles provides a ready to use 2G cellular module embedding a SIM card with a prepaid data plan, valid for a long-term period up to the year 2025, at no additional cost and without any monthly fee.

The device is utilizing Orange's high-quality cellular network and a large tier one roaming network covering 33 European countries through an extensive count of operators.

The certified Heracles module offers a most miniaturized solution, ideal for ultra-compact designs and a best in class low power SoC. Due to its very small size of 15.8 x 17.8 x 2.4 mm<sup>3</sup> it fits into slim and compact designs fulfilling the demands of any customer project.

Heracles supports quad-band 850/900/1800/1900 MHz, GPRS multi-slot class 12/10, as well as GPRS mobile station class B and is compliant to GSM phase 2/2+ Class 4 and 1.

The module's high quality and small form factor make it ideal for SMD assembly.

### KEY FEATURES

#### General

- Quad-band 850/900/1800/1900 MHz
- GPRS multi-slot class 12/10 & mobile station class B
- GSM phase 2/2+ compliant
  - Class 4 (2 W @ 850/900 MHz)
  - Class 1 (1 W @ 1800/1900 MHz)
- Embedded industrial temperature SIM card
- 15.8 x 17.8 x 2.4 mm<sup>3</sup>
- AT cellular command interface
- Weight: 1.5 g
- Supply voltage range 3.4...4.4 V
- Low power consumption
- Operating temperature: -40...-85 °C

#### Specifications for GPRS Data

- GPRS class 12: max. 85.6 kbps (downlink/uplink)
- PBCCH support
- Coding schemes CS 1,2,3,4
- PPP-Stack

#### Software Features

- 07/10 MUX protocol
- Embedded TCP/UDP
- FTP/HTTP
- E-Mail
- DTMF
- Jamming Detection
- Audio Record
- SSL

#### Interfaces

- 88 LGA pads including:
- Analog audio interface
- PCM , USB, Keypad interface
- RTC Backup, GPIO, ADC
- GSM Antenna pad

#### Certifications

- CE, RoHS, REACH, RED, Orange Cellular Network

#### Prepaid Data plans guaranteed until 2025:

- Small: 10 MB
- Medium: 40 MB
- Large: 200 MB
- Extra-Large: 500 MB

#### Coverage in over 33 countries without roaming fees:

Austria	Germany	Netherlands
Andorra	Greece	Norway
Belgium	Hungary	Poland
Bulgaria	Iceland	Portugal
Croatia	Ireland	Romania
Cyprus	Italy	Slovakia
Czech Republic	Lativa	Slovenia
Denmark	Liechtenstein	Spain
Estonia	Lithuania	Sweden
Finland	Luxembourg	Switzerland
France	Malta	United Kingdom

### APPLICATION EXAMPLES

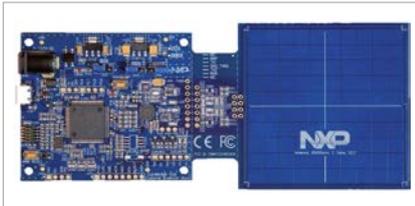
- Location tracking
- Automotive (aftermarket)
- Wearables
- IoT applications
- M2M applications
- Industrial applications

# CLRC663 PLUS

Push your NFC design further with NXP's next-gen multi-protocol NFC frontend



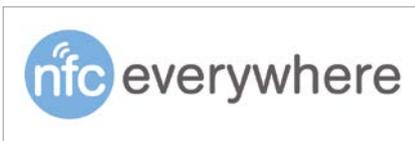
CLICK OR SCAN



CLEV6630B

**The CLRC663 plus is the perfect choice for NFC applications with high performance requirements.**

If you need excellent NFC performance or ultra-low power consumption, use this remarkably efficient yet highly flexible frontend to push your design further. It offers an extended temp range, pin-to-pin compatibility to CLRC663 family (MFRC630, SLRC610), and time-saving software tools.



The CLRC663 plus is a great choice for NFC applications with high performance requirements: access control, payment, gaming and industrial.

## KEY FEATURES

- High performance and more flexible antenna design
  - Maximum transmitter current: 350 mA operating with 500 mA limiting value
  - Freely programmable 6 kByte EEPROM
- Longer battery life
  - Supply voltage: 2.5...5.5 V
  - Power-save modes: hard power-down, standby, extended LPCD options
- Industrial/Automotive temp range (-40...+105 °C)
- Multiple interfaces to support a broad range of microcontrollers and high-security reader implementations
  - Host interfaces: SPI, I<sup>2</sup>C, UART
  - Up to 8 GPIO
  - SAM interface
  - 512 byte FIFO buffer reduces performance requirements of host controller
- Fast development
  - Supports NFC Cockpit and NFC Reader Library
  - Complete development kits
- Included licenses
  - ISO/IEC14443-A/B, MIFARE Classic encryption in hardware with Crypto 1 IP licensing rights
- EMVCo ready
  - EMVCo 2.6 L1 analog & digital compliance
- Full RF standard compliance
  - ISO/IEC 14443A/MIFARE
  - ISO/IEC 14443B
  - JIS X 6319-4 (comparable with FeliCa1 scheme)
  - ISO/IEC 15693 (ICODE-SLIX and ICODE-DNA)
  - ISO/IEC 18000-3 mode 3 (ICODE-ILT)
  - Peer-to-Peer Mode: ISO/IEC 18092 passive initiator
- Compatible with all NXP smartcard products
  - Complete MIFARE® family: Ultralight, Classic 1K & 4K, DESFire EV1 & EV2 and Plus EV1
  - Complete NTAG® family incl. NTAG I<sup>2</sup>C plus
  - Complete SmartMX® family incl. SmartMX2 P40 & P60
- Compact, time-saving package
  - HVQFN32 with wettable flanks to support high production yield
  - Pin-compatible to CLRC663 makes it easy to upgrade from existing designs

## APPLICATION EXAMPLES

- Industrial
- Access control
- Payment
- Gaming

### Comparison: CLRC663 versus CLRC663 plus

	CLRC663	CLRC663 plus
Operating transmitter current	250 mA	350 mA (max.), 500 mA (lim.)
LPCD range (EMVCo RefPICC)	26 mm	66 mm
Temp range	-25...+85 °C	-40...+105 °C
RF transmitter supply voltage	3.0...5.5 V	2.5...5.5 V
Package	HVQFN32	HVQFN32 with wettable flanks

# SIP-0P5WRS301

## Samsung ARTIK 053 WiFi Based IoT Module



CLICK OR SCAN



Samsung ARTIK 053 Module Size Comparison



Samsung ARTIK 053 Module

**The Samsung ARTIK Smart IoT platform brings hardware modules and cloud services together with an ecosystem of tools and partners to speed up your time-to-market.**

The ARTIK 053 is a WiFi-based IoT module with built-in hardware security for low power, single function "things". It features a 320 MHz, 32-bit ARM® Cortex®-R4 and WiFi connectivity. It runs the Tizen RT real-time operating system and makes use of open source development tools.

ARTIK 053 brings Wi-Fi to things that need connectivity and low power consumption, but without sacrificing security. ARTIK 053 has a built-in security module that keeps its factory-installed certificates and keys safe.

ARTIK 053 runs Tizen RT, a compact RTOS with built-in support for IoTivity, Lightweight machine-to-machine (LWM2M) protocol, and

JerryScript/IoT.js. This also means you can develop for ARTIK 053 using open source tools like Eclipse Classic Desktop (CDT), gcc, and OpenOCD.

### KEY FEATURES

#### Performance and flexibility

- 32-bit ARM® Cortex®-R4 @ 320 MHz for applications
- 29 dedicated GPIO ports, 2 SPI, 4 UART (2-pint), 4 ADC, 1 JTAG, 2 I<sup>2</sup>C
- 5...12 VDC input voltage

#### Integrated security subsystem

- Secure cloud communication
- Secure bootSecure storage

#### Integrated and tested middleware

- Tizen RT with RTOS, Wi-Fi and networking software
- API interface to simplify development process
- LWM2M support

#### End-to-end with ARTIK Cloud

- Mobile app to add modules to ARTIK Cloud
- Manage devices, including OTA updates, with ARTIK Cloud

### APPLICATION EXAMPLES

- Smart Lighting
- Industrial
- Retail
- Smart Building
- Manufacturing



100-up price  
SIP-005AFS301  
**€ 38.50**

## SIP-005AFS301

# Samsung ARTIK 530 802.11 Bluetooth® ZigBee Thread Module



CLICK OR SCAN



Samsung ARTIK 530 Module

**Samsung ARTIK Smart IoT platform brings hardware modules and cloud services together with an ecosystem of tools and partners to speed up your time-to-market.**

ARTIK 530 takes ARTIK 520's combination of power and multimedia and adds power while reducing cost.

We've bumped the dual-core processor up to a quad core, and replaced the connector-based interface with a solder-ball pattern, saving you BOM costs as well. Although the footprint is larger, you still get amazing multimedia capabilities and connectivity options.

The ARTIK 530 features a Quad-core processor for local data processing and a multimedia engine to handle audio and video processing. The modules also support most of the commonly used local area wireless standards such as 802.11, Bluetooth®, ZigBee®, and Thread.

### KEY FEATURES

- High performance, 4-core, 32-bit ARM® Cortex® A-9 processor @ 1.2 GHz
- ARM MALI™ GPU for multimedia, graphics applications
- 512 MB RAM, 4 GB flash (eMMC)
- Support for WLAN, Bluetooth®, ZigBee, Thread
- Enterprise-class security with hardware secure element and Secure OS
- Fedora Linux package with multimedia, connectivity, graphics, power management and security libraries

### APPLICATION EXAMPLES

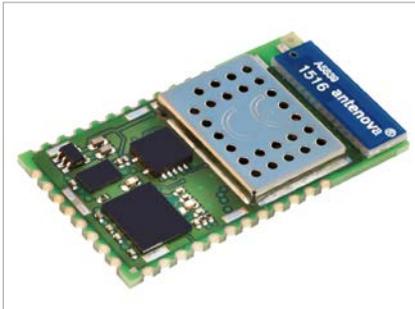
- Factory automation
- Smart home gateway
- Building Automation controllers
- Multimedia applications

# SPWF04SX

## WiFi Module With and Without Integrated Antenna



CLICK OR SCAN



SPWF04SX



X-NUCLEO-IDW04A1

**The SPWF04SA and the SPWF04SC are ready-to-use Wi-Fi modules conceived for Internet of Things (IoT) applications.**

The modules integrate a Cortex-M4-based STM32 microcontroller and a powerful Wi-Fi transceiver, compliant with IEEE 802.11 b/g/n standard for the 2.4 GHz band. The modules are cloud compatible thanks to the complete protocol package, which includes application and security layers. They can operate in serial-to-Wi-Fi and standalone mode, with customer applications developed in the subsystem based on the MicroPython environment.

The modules are configured around a single-chip 802.11 transceiver with integrated PA and comprehensive power management subsystem, and an STM32F4 microcontroller with UART/SPI interface and an extensive GPIO suite; they also incorporate timing clocks and a voltage regulator.

The SPWF04Sx parts are released with an integrated full-featured TCP/IP protocol stack with added web

server and additional application service capabilities, including REST API for accessing files on servers in the cloud and support for dynamic web pages with SSI functions to easily interact with the module and the host processor over the air. Application note AN4965 on [www.st.com](http://www.st.com) provides details on HTTP server capabilities.

ST may update the module firmware at any time; check regularly for documentation and firmware updates on [www.st.com/wifimodules](http://www.st.com/wifimodules).

### KEY FEATURES

- 2.4 GHz IEEE 802.11 b/g/n low power transceiver
- STM32 ARM® Cortex®-M4 with 2 MB Integrated Flash memory
- Certified RF (FCC, IC, CE)
- UART/SPI-to-WiFi or standalone supported with MicroPython script language
- TLS for End-to-End security integrated in all modules
- Security: WPS, WEP, WPA2, WPA-Enterprise

- HTTPS, MQTT, SMTP, WebSockets, IPv6 protocols and to easily connect applications to the cloud
- Over-The-Air firmware update
- System Modes: mini-AP mode, IBSS and Station
- Size: 26.92 × 15.24 × 2.35 mm<sup>3</sup>
- Advanced low-power modes
  - Standby with RTC: 43 μA
  - Sleep = 3 mA
  - Idle connected (DTIM=1) = 5 mA
  - RX traffic 105 mA typical
  - TX traffic 260 mA typical @ 10 dBm
- Single voltage supply (3.3 V typical)
- Industrial temperature range: -40... 85° C

### APPLICATION EXAMPLES

- Smart appliances
- Industrial control and data acquisition
- Home automation and security systems
- Wireless sensors
- Cable replacement
- Medical equipment
- Machine-to-machine communication

## EBV EUROPEAN HEADQUARTERS

EBV Elektronik GmbH & Co. KG | DE-85586 Poing | Im Technologiepark 2-8 | Phone: +49 8121 774 0 | www.ebv.com

## EBV REGIONAL OFFICES | Status October 2017

### AUSTRIA

AT-1120 Wien  
Grünbergstraße 15 / Stiege 1 / 7. OG  
Phone: +43 1 89152 0  
Fax: +43 1 89152 30

### BELGIUM

BE-1831 Diegem  
Kouterveldstraat 20  
Phone: +32 2 716001 0  
Fax: +32 2 72081 52

### BULGARIA

BG-1505 Sofia  
48 Sitnyakovo Blvd., Serdika  
offices, 10<sup>th</sup> floor, Unit 1006  
Phone: +359 2 9264 337  
Fax: +359 2 9264 133

### CZECH REPUBLIC

Amazon Court  
Karolinska 661/4  
CZ-18600 Prague  
Czech Republic  
Phone: +420 2 34091 011  
Fax: +420 2 34091 010

### DENMARK

DK-8230 Åbyhøj  
Ved Lunden 10-12, 1. sal  
Phone: +45 8 6250 466  
Fax: +45 8 6250 660

DK-2730 Herlev  
Lyskær 9, 1. sal  
Phone: +45 39 6905 11  
Fax: +45 39 6905 04

### ESTONIA

EE-10414 Tallinn  
Niine 11  
Phone: +372 62 5799 0  
Fax: +372 62 5799 5  
Cell: +372 513 2232

### FINLAND

FI-02240 Espoo  
Pihatörmä 1 a  
Phone: +358 9 2705279 0  
Fax: +358 9 2705498

FI-90100 Oulu  
Nahkatehtaankatu 2  
Phone: +358 8 4152627 0  
Fax: +358 8 4152627 5

### FRANCE

FR-13856 Aix-en-Provence  
1330 Rue G.G. de la Lauziere  
Europarc Pichaury, Bâtiment A2  
Phone: +33 442 3965 40  
Fax: +33 442 3965 60

FR-92184 Antony Cedex (Paris)  
2-6 Place Du General De Gaulle -  
CS70046  
Phone: +33 1 409630 00  
Fax: +33 1 409630 30

FR-35510 Cesson Sévigné (Rennes)  
35, av. des Peupliers  
Phone: +33 2 998300 50  
Fax: +33 2 998300 60

FR-67400 Illkirch Grafenstaden  
35 Rue Gruningger  
Phone: +33 3 904005 92  
Fax: +33 3 886511 25

FR-31500 Toulouse  
8 chemin de la terrasse  
Parc de la plaine  
Phone: +33 5 610084 61  
Fax: +33 5 610084 74

FR-69693 Venissieux (Lyon)  
Parc Club du Moulin à Vent  
33, Av. du Dr. Georges Lévy  
Phone: +33 4 727802 78  
Fax: +33 4 780080 81

### GERMANY

DE-85609 Aschheim-Dornach  
Einsteinring 1  
Phone: +49 89 38882 351  
Fax: +49 89 38882 444

DE-10587 Berlin  
Englische Straße 28  
Phone: +49 30 747005 0  
Fax: +49 30 747005 55

DE-30938 Burgwedel  
Burgdorfer Straße 2  
Phone: +49 5139 8087 0  
Fax: +49 5139 8087 70

DE-59439 Holzwickede  
Wilhelmstraße 1  
Phone: +49 2301 94390 0  
Fax: +49 2301 94390 30

DE-41564 Kaarst  
An der Gumpgesbrücke 7  
Phone: +49 2131 9677 0  
Fax: +49 2131 9677 30

DE-71229 Leonberg  
Neue Ramtelstraße 4  
Phone: +49 7152 3009 0  
Fax: +49 7152 759 58

DE-90471 Nürnberg  
Lina-Ammon-Straße 19B  
Phone: +49 911 817669 0  
Fax: +49 911 817669 20

DE-04435 Schkeuditz  
Airport Business Center Leipzig  
Frankfurter Straße 2  
Phone: +49 34204 4511 0  
Fax: +49 34204 4511 99

DE-78048 VS-Villingen  
Marie-Curie-Straße 14  
Phone: +49 7721 99857 0  
Fax: +49 7721 99857 70

DE-65205 Wiesbaden  
Borsigstraße 36  
Phone: +49 6122 8088 0  
Fax: +49 6122 8088 99

### HUNGARY

HU-1117 Budapest  
Budafoki út 91-93, West Irodaház  
Phone: +36 1 43672 29  
Fax: +36 1 43672 20

### IRELAND

IE-Dublin 12  
Calmount Business Park  
Unit 7, Block C  
Phone: +353 1 40978 02  
Fax: +353 1 45685 44

### ISRAEL

IL-40600 Tel Mond  
Drorim South Commercial Center  
P.O. Box 149  
Phone: +972 9 77802 60  
Fax: +972 3 76011 15

### ITALY

IT-20092 Cinisello Balsamo (MI)  
Via C. Fropa, 34  
Phone: +39 02 660962 90  
Fax: +39 02 660170 20

IT-50019 Sesto Fiorentino (FI)  
EBV Elektronik Srl  
Via Lucchese, 84/B  
Phone: +39 05 543693 07  
Fax: +39 05 542652 40

IT-41126 Modena (MO)  
Via Scaglia Est, 33  
Phone: +39 059 292 4211  
Fax: +39 059 292 9486

IT-80128 Napoli (NA)  
Via G. Capaldo, 10  
Phone: +39 081 193016 03  
Fax: +39 081 198061 24  
Cell: +39 335 83905 31

IT-00155 Roma (RM)  
Via Edoardo D'Onofrio 212  
Phone: +39 06 4063 665/778  
Fax: +39 06 4063 777

IT-35030 Sarmeola di Rubano (PD)  
Piazza Adelaide Lonigo, 8/11  
Phone: +39 049 89747 01  
Fax: +39 049 89747 26

IT-10144 Torino (TO)  
Via Treviso, 16  
Phone: +39 011 26256 90  
Fax: +39 011 26256 91

### NETHERLANDS

NL-3606 AK Maarssenbroek  
Planetenbaan 116  
Phone: +31 346 5830 10  
Fax: +31 346 5830 25

### NORWAY

Postboks 101, Manglerud  
Ryensvingen 3B  
NO-0681 Oslo  
Phone: +47 22 67178 0  
Fax: +47 22 67178 9

### POLAND

PL-80-833 Gdansk  
Targ Rybny 11/12  
Phone: +48 58 30781 00

PL-02-674 Warszawa  
Ul. Marynarska 11  
Phone: +48 22 25747 06

PL-50-062 Wrocław  
Pl. Solny 16  
Phone: +48 71 34229 44  
Fax: +48 71 34229 10

### PORTUGAL

Unipessoal LDA  
Edifício Tower Plaza  
Rotunda Eng.º Edgar Cardoso, 23 - 14ºG  
PT-4400-676 Vila Nova de Gaia  
Phone: +351 22 092026 0  
Fax: +351 22 092026 1

### ROMANIA

4C Gara Herastrai Street  
Building B, 2<sup>nd</sup> Floor - 2<sup>nd</sup> District  
Bucharest  
RO 014472  
Phone: +40 21 52816 12  
Fax: +40 21 52816 01

### RUSSIA

RU-620028 Ekaterinburg  
Tatischeva Street 49A  
Phone: +7 343 31140 4  
Fax: +7 343 31140 46

RU-127486 Moscow  
Korovinskoye Shosse 10,  
Build 2, Off.28  
Phone: +7 495 730317 0  
Fax: +7 495 730317 1

RU-195197 St. Petersburg  
Pulostrovsky Prospect 43,  
Office 421  
Phone: +7 812 635706 3  
Fax: +7 812 635706 4

### SERBIA

Balkanska 2  
XS-11000 Belgrade  
Phone: +381 11 40499 01  
Fax: +381 11 40499 00  
Mobile: +381 63 204506  
Mobile: +381 62 780012

### SLOVAKIA

SK-82109 Bratislava  
Turčianska 2  
Green Point Offices  
Phone: +421 2 3211114 1  
Fax: +421 2 3211114 0

### SLOVENIA

SI-1000 Ljubljana  
Dunajska 167  
Phone: +386 1 5609 778  
Fax: +386 1 5609 877

### SOUTH AFRICA

ZA-8001 Foreshore, Cape Town  
1 Mediterranean Street  
5th Floor MSC House  
Phone: +27 21 402194 0  
Fax: +27 21 4196256

ZA-3629 Westville  
Forest Square, 11 Derby Place  
Suite 4, Bauhinia Building  
Phone: +27 31 27926 00  
Fax: +27 31 27926 24

ZA-2157 Woodmead,  
Johannesburg  
Woodlands Office Park  
141 Western Service Road  
Building 14-2nd Floor  
Phone: +27 11 23619 00  
Fax: +27 11 23619 13

### SPAIN

ES-08014 Barcelona  
c/Tarragona 149 - 157 Planta 19 1º  
Phone: +34 93 47332 00  
Fax: +34 93 47363 89

ES-39005 Santander (Cantabria)  
Racing nº 5 bajo  
Phone: +34 94 22367 55  
Phone: +34 94 23745 81

ES-28760 Tres Cantos (Madrid)  
Centro Empresarial Euronova  
C/Ronda de Poniente, 4  
Phone: +34 91 80432 56  
Fax: +34 91 80441 03

### SWEDEN

SE-191 62 Sollentuna  
Glimmervägen 14, 7 tr  
Phone: +46 859 47023 0  
Fax: +46 859 47023 1

### SWITZERLAND

CH-8953 Dietikon  
Bernstrasse 394  
Phone: +41 44 74561 61  
Fax: +41 44 74561 00

CH-1010 Lausanne  
Av. des Boveresses 52  
Phone: +41 216 5401 01  
Fax: +41 216 5401 00

### TURKEY

Canan Residence  
Hendem Cad. No: 54 Ofis A2  
Serifali Umraniye  
TR-34775 Istanbul  
Phone: +90 216 528831 0  
Fax: +90 216 528831 1

Armada Is Merkezi  
Eskisehir Yolu No: 6 , Kat: 1406  
Ofis No: 1406  
Sogutozu  
TR-06520 Ankara  
Phone: +90 312 2956 361  
Fax: +90 312 2956 200

### UKRAINE

UA-03040 Kiev  
Vasilovskaya str. 14  
off. 422-423  
Phone: +380 44 496222 6  
Fax: +380 44 496222 7

### UNITED KINGDOM

South East  
2, The Switchback  
Gardner Road  
Maidenhead  
GB-Berkshire, SL6 7RJ  
Phone: +44 16 28778556  
Fax: +44 16 28783811

South West & Wales  
12 Interface Business Park  
Binknoll Lane  
Royal Wootton Bassett  
GB-Wiltshire, SN4 8SY  
Phone: +44 17 93849933  
Fax: +44 17 93859555

North  
Manchester International  
Office Centre, Suite 3E (MIOC)  
Styal Road  
GB-Manchester, M22 5WB  
Phone: +44 16 149934 34  
Fax: +44 16 149934 74

Scotland  
1<sup>st</sup> Floor  
180 St. Vincent Street  
GB-Glasgow, G2 5SG  
Phone: +44 141 242482 0  
Fax: +44 141 2211916

