

Zynq®-7000 SoC and Zynq® UltraScale+™ MPSoC Systems

From Concept to Production



All statements are without any engagement. Subject to modifications and amendments. | P-323-E-11-2018-v1

Zyng®-7000 SoC and Zyng® UltraScale+™ MPSoC Systems

From Concept to Production

DESIGN IT OR BUY IT?

Avnet's ready-made SoC modules can shorten your development cycle

Today's quick time-to-market demands are forcing you to rethink how you design, build and deploy your products. Sometimes it's faster, less costly, and lower risk to incorporate an off-the-shelf solution instead of designing from the beginning. Avnet's System-On-Module (SOM) and Single-Board Computer (SBC) solutions for the Xilinx Zynq®-7000 SoC and Zynq UltraScale+ MPSoC SoC can reduce development times by more than four months, allowing you to focus your efforts on adding differentiating features and unique capabilities.

Avnet's SoC Modules Offer the Following Benefits:

- Reduce risk by building your application upon a known working system
- Get running quickly with example designs, tutorials, and board support packages
- · Start software development immediately

With now over fifteen years of experience building SOM products, we've helped many companies attain a jump start on their products and get to market faster. Contact us today to get started!

Avnet's Zynq UltraScale+ MPSoC SOM Solutions

Features	UltraZed-EG			UltraZed-EV						
	ZU2CG	ZU3CG	ZU2EG	ZU3EG ¹	ZU4EG	ZU4EV	ZU5EG	ZU5EV	ZU7EG	ZU7EV
Zynq Device	ZU2CG-1	ZU3CG-1	ZU2EG-1	ZU3EG-1	ZU4EG-1	ZU4EV-1	ZU5EG-1	ZU5EV-1	ZU7EG-1	ZU7EV-1
Programmable Logic Cells	103 K	154 K	103 K	154 K	192 K	192 K	256 K	256 K	504 K	504 K
DDR Memory	2 GB DDR4	2 GB DDR4	2 GB DDR4	2 GB DDR4	5 GB DDR4	5 GB DDR4	5 GB DDR4	5 GB DDR4	5 GB DDR4	5 GB DDR4
QSPI	512 Mb	512 Mb	512 Mb	512 Mb	512 Mb	512 Mb				
uSD Card Cage	No	No	No	No	No	No	No	No	No	No
eMMC Memory	8 GB	8 GB	8 GB	8 GB	8 GB	8 GB				
User I/O	180/26 ²	180/26 ²	180/26 ²	180/26 ²	152/26 ²	152/26 ²	152/26 ²	152/26 ²	152/26 ²	152/26 ²
GTP/GTX/GTR Ports	4 PS	4 PS	4 PS	4 PS	4 PS/16 PL					
10/100/1000 Ethernet	Yes	Yes	Yes	Yes	Yes	Yes	Yes	Yes	Yes	Yes
USB 2.0	Yes	Yes	Yes	Yes	Yes	Yes	Yes	Yes	Yes	Yes
USB-UART	No	No	No	No	No	No	No	No	No	No
Other Peripherals	2 Kb EEPROM	2 Kb EEPROM	2 Kb EEPROM	2 Kb EEPROM	2 Kb EEPROM	2 Kb EEPROM				
Size	3.5" x 2" 89 x 51 mm	4" x 2.5" 102 x 63.5 mm								

- 1. Zynq: PL IO / PS MIO
- 2. Resale based on 1k units (commercial grade)

Pmod is a registered trademark of Digilent

Avnet's Zynq-7000 SoC SOM Solutions

Features	PicoZed				MicroZed	
	7010	7015	7020	7030	7010	7020
Zynq Device	7Z010-1	7Z015-1	7Z020-1	7Z030-1	7Z010-1	7Z020-1
Programmable Logic Cells	28 K	74 K	85 K	125 K	28 K	85 K
DDR Memory	1 GB DDR3L	1 GB DDR3L	1 GB DDR3L	1 GB DDR3L	1 GB DDR3	1 GB DDR3
QSPI	128 Mb					
uSD Card Cage	No	No	No	No	Yes	Yes
eMMC Memory	8 GB	8 GB	8 GB	8 GB	No	No
User I/O	100/13 ²	135/13 ²	125/13 ²	135/13 ²	100/82	115/8 ²
GTP/GTX/GTR Ports	-	4	-	4	-	-
10/100/1000 Ethernet	Yes	Yes	Yes	Yes	Yes	Yes
USB 2.0	Yes	Yes	Yes	Yes	Yes	Yes
USB-UART	No	No	No	No	Yes	Yes
Other Peripherals	-	-	-	-	$Pmod^{TM}$	$Pmod^{\scriptscriptstyleTM}$
Size	4" x 2.25" 102 x 57 mm					

^{1.} Zynq: PL IO / PS MIO

Pmod is a registered trademark of Digilent

Custom SOM Offerings

Customize the module with AVID – an Avnet Company with extensive experience designing and customizing single board computer platforms. Email us at customize@avnet.com to explore the options.

^{2.} Resale based on 1k units (commercial grade)

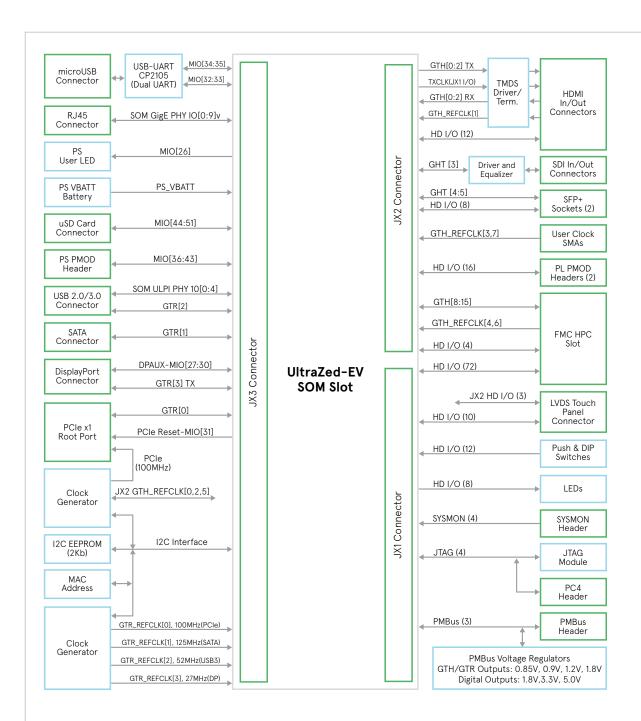
UltraZed-EV™

UltraZed-EV™ SOM is a high performance, full-featured, System-On-Module (SOM) based on the Xilinx Zynq® UltraScale+™ MPSoC EV family of devices. Designed in a small form factor, the UltraZed-EV SOM on-board dual system memory, high-speed transceivers, Ethernet, USB, and configuration memory provides an ideal platform for embedded video processing systems. The UltraZed-EV provides easy access to 152 user I/O pins, 26 PS MIO pins, 4 highspeed PS GTR transceivers along with 4 GTR reference clock inputs, and 16 PL high-speed GTH transceivers along with 8 GTH reference clock inputs through three I/O connectors on the backside of the module. These connectors provide USB 2.0, USB 3.0, PCle Gen2, DisplayPort, SATA 3.0, FMC-HPC and more! The MPSoC EV device with its integrated H.264 / H.265 video codec unit is capable of simultaneous encode and decode up to 4Kx2K (60fps).



FEATURES

- Xilinx XCZU7EV-1FBVB900 device (SOM also supports 4EV, 5EV, 4EG, 5EG, or 7EG device in the FBVB900 package)
- PS DDR4 SDRAM (4GB, in x64 configuration)
- PL DDR4 SDRAM (1GB, in x16 configuration)
- Dual QSPI Flash (64MB)
- I2C EEPROM (2Kb)
- eMMC Flash (8GB, x8)
- USB 2.0 ULPI PHY
- Ethernet PHY
- PS reference clock input
- 3 JX connectors, providing
 - PS JTAG interface
 - PL SYSMON interface
 - Gigabit Ethernet RJ45 connector interface
 - PMBus interface
 - Power Good output, input voltages, and output sense pins)



Additional information and downloadable documentation for UltraZed-EV can be obtained at avnet.me/ultrazed-ev

Description
UltraZed-EV SOM (Extended Temp)
UltraZed-EV SOM (Industrial Temp)

UltraZed-EG™

UltraZed-EG[™] SOM is a highly flexible, rugged, System-On-Module (SOM) based on the Xilinx Zynq® UltraScale+™ MPSoC. Designed in a small form factor, the UltraZed-EG SOM packages all the necessary functions such as system memory, Ethernet, USB, and configuration memory needed for an embedded processing system. The UltraZed-EG provides easy access to 180 user I/O pins, 26 PS MIO pins, and 4 high-speed PS GTR transceivers along with 4 GTR reference clock inputs through three I/O connectors on the backside of the module.

Designers can simply design their own carrier card, plug-in UltraZed-EG SOM, and start their application development with a proven Zynq UltraScale+ MPSoC sub-system. Available with the Zynq UltraScale+ MPSoC XCZU3EG-SFVA625 device, the UltraZed-EG SOM enables designers to build high-performance systems with confidence and ease. By simply plugging the off-the-shelf UltraZed-EG SOM into an application specific carrier card, system bring-up and debug time can be cut in half, while overall system cost can be reduced by 20% or more verses a standard chip-down design.



FEATURES

MPSoC

- Xilinx XCZU3EG-1SFVA625 device
- Other options are available with MOQ=100

Memory

- DDR4 SDRAM (2GB, in x32 configuration)
- Dual QSPI Flash (64MB)
- I2C EEPROM (2Kb)
- eMMC Flash (8GB, in x8 configuration)

Communications

- USB 2.0 ULPI PHY
- Gigabit Ethernet PHY

Other

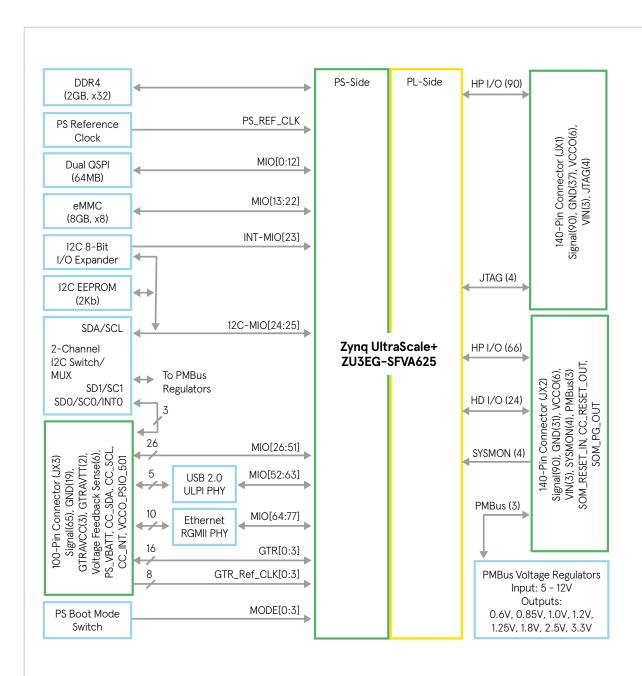
- PS reference clock input
- On-board voltage regulators
- · Power-On Reset (POR) circuit
- · 4-position boot mode slide switch
- · Heatsink included

Software

Linux BSP and reference designs

User I/O (via three board-to-board connectors)

- 3 JX micro-header connectors (2 x 140-pin, 1 x 100-pin) providing the following connections to the Carrier Cards
 - 180 user PL I/O pins
 - 26 user PS MIO pins (one full MIO bank)
 - 4 PS GTR transceivers
 - 4 PS GTR reference clock inputs
 - PS JTAG interface
 - PL SYSMON interface
 - USB 2.0 connector interface
 - PMBus interface
 - Carrier Card I2C interface
 - SOM Reset input
 - Carrier Card interrupt input
 - Carrier Card Reset output
 - Power Good output



Additional information and downloadable documentation for UltraZed can be obtained at avnet.me/ultrazed-eg

Part number	Description
AES-ZU3EG-1-SOM-I-G	UltraZed-EG SOM

Zyng® Mini-ITX

The Zynq®-7000 SoC Mini-ITX platform provides an industry standard, motherboard form-factor for designers seeking a high performance platform based on the Xilinx Zynq-7000 SoC. Available with either the 7Z045 or 7Z100 SoC, the Mini-ITX offers the unique mix of user configurability, expandability, and standard interfaces that designers are looking for.



FEATURES

SoC

- XC7Z045-2FFG900
- XC7Z100-2FFG900

Memory

- 1 GB of DDR3 SDRAM (PS side)
- 1 GB of DDR3 SDRAM (PL side)
- 256 Mb of QSPI Flash
- 8 KB of I2C EEPROM
- MicroSD card interface

Communications

- 10/100/1000 Ethernet
- SFP+ socket
- 4-Port USB 2.0 hub
- USB-UART
- SATA-III interface

Expansion

- PCle Gen2 x4 electrical, x16 mechanical slot (Root Complex)
- FMC slot (HPC)

Other

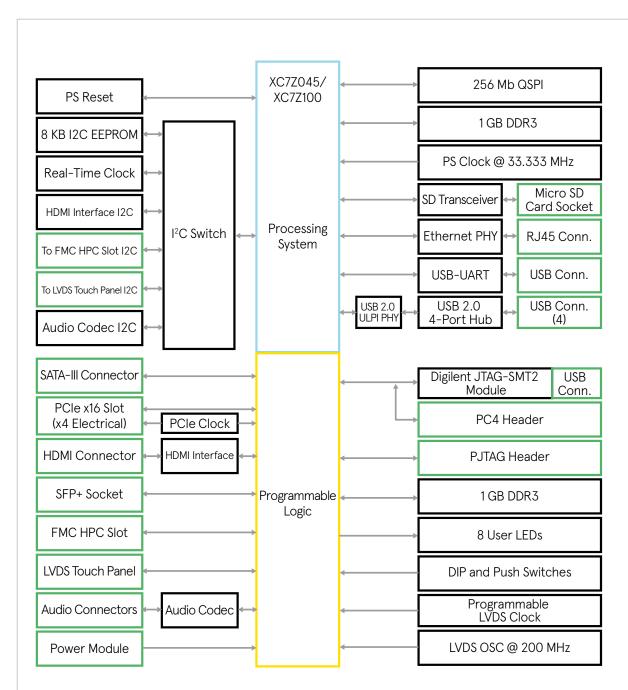
- HDMI output port
- LVDS LCD panel interface
- Audio input and output
- Real-time clock
- Programmable GTX reference clock
- 200 MHz LVDS oscillator
- 33.33 MHz oscillator
- Xilinx PC4 JTAG configuration port
- Processor PJTAG header
- 8 User LEDs
- Push buttons and slide switches
- Standard ATX power connector input

Software

• Linux BSP and reference designs

Mechanical

4 inches x 2.25 inches (102 mm x 57 mm)



Additional information and downloadable documentation for the Zynq Mini-ITX can be obtained at avnet.me/mini-itx

PARTS Part number

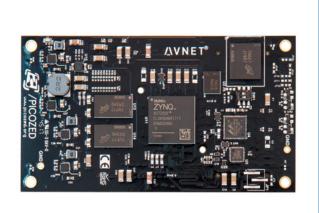
Part number	Description
AES-MINI-ITX-7Z045-G	Z045 Zynq Mini-ITX
AES-MINI-ITX-7Z100-G	7Z100 Zynq Mini-ITX

PicoZed™

PicoZed[™] is a highly flexible, rugged SOM that is based on the Xilinx Zynq-7000 SoC. It offers designers the flexibility to migrate between the 7010, 7015, 7020, and 7030 Zynq-7000 SoC devices in a pin-compatible footprint.

The PicoZed module contains the common functions required to support the core of most SoC designs, including memory, configuration, Ethernet, USB, clocks, and power. It provides easy access to over 100 user I/O pins through three I/O connectors on the backside of the module. These connectors also support access to dedicated interfaces for Ethernet, USB, JTAG, power and other control signals, as well as the GTP/GTX transceivers on the 7015/7030 models. The transceiver based 7015 and 7030 versions of PicoZed are a superset of the 7010/7020 version, adding four highspeed serial transceiver ports to the I/O connectors. Designers can simply design their own carrier card, plug-in PicoZed, and start their application development with a proven Zynq-7000 SoC sub-system.

Industrial Temperature PicoZed SOMs are built with components supporting extended temperatures of -40 to +85°C. Due to the configurability of the Zynq device, the user must perform final temperature testing validation.



FEATURES

SoC options

- XC7Z010-1CLG400
- XC7Z015-1CLG485
- XC7Z020-1CLG400
- XC7Z030-1SBG485

Memory

- 1 GB of DDR3L SDRAM
- 8 GB eMMC
- 128 Mb of QSPI Flash

Communications

- 10/100/1000 Ethernet PHY
- USB 2.0 OTG PHY

User I/O (via three board-to-board connectors)

- 7Z010 Version
 - 113 User I/O (100 PL, 13 PS MIO)
 - PL I/O configurable as up to 48 LVDS pairs or 100 single-ended I/O
- 7Z015 Version
 - 148 User I/O (135 PL, 13 PS MIO)
 - PL I/O configurable as up to 65 LVDS pairs or 135 single-ended I/O
 - 4 GTP Transceivers
- 7Z020 Version
 - 138 User I/O (125 PL, 13 PS MIO)
 - PL I/O configurable as up to 60 LVDS pairs or 125 single-ended I/O
- 7Z030 Version
 - 148 User I/O (135 PL, 13 PS MIO)
 - PL I/O configurable as up to 65 LVDS pairs or 135 single-ended I/O
 - 4 GTX Transceivers

Other

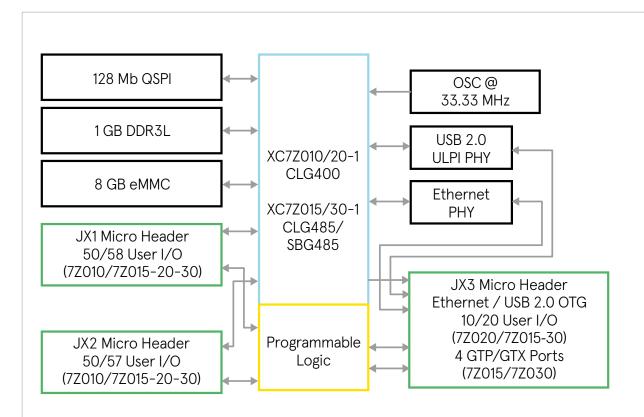
- JTAG configuration port accessible via I/O connectors
- PS JTAG pins accessible via I/O connectors
- 33.33 MHz oscillator

Software

· Linux BSP and reference designs

Mechanical

4 inches x 2.25 inches (102 mm x 57 mm)



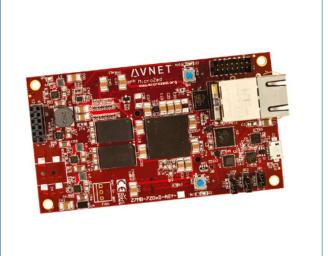
Additional information and downloadable documentation for PicoZed can be obtained at avnet.me/picozed.

Part number	Description
AES-Z7PZ-7Z010-SOM-G	7010 PicoZed SOM
AES-Z7PZ-7Z010-SOM-I-G	7010 Ind. Temp PicoZed SOM
AES-Z7PZ-7Z015-SOM-I-G	7015 Ind. Temp PicoZed SOM
AES-Z7PZ-7Z020-SOM-G	7020 PicoZed SOM
AES-Z7PZ-7Z020-SOM-I-G	7020 Ind. Temp PicoZed SOM
AES-Z7PZ-7Z030-SOM-I-G	7030 Ind. Temp PicoZed SOM

MicroZedTM

MicroZed[™] is a low-cost SOM that is based on the Xilinx Zynq®-7000 SoC. In addition to the Zynq-7000 SoC, the module contains the common functions and interfaces required to support the core of most SoC designs, including memory, configuration, Ethernet, USB, and clocks. On the bottom side of the module, MicroZed contains two 100-pin I/O headers that provide connection to two I/O banks on the programmable logic (PL) side of the Zynq-7000 SoC device. When plugged onto a user designed baseboard or carrier card, these 100-pin connectors provide connectivity between the Zynq-7000 SoC PL I/Os and the user circuits on the carrier card. MicroZed also includes on-board power regulation that supports 5 V input with an option to support 12 V input.

Industrial Temperature MicroZed SOMs are built with components supporting extended temperatures of -40 to +85°C, with the exception of the use of the microSD card connector. Due to the configurability of the Zynq device, the user must perform final temperature testing validation.



FEATURES

SoC

- XC7Z010-1CLG400 or
- XC7Z020-1CLG400

Memory

- 1 GB of DDR3 SDRAM
- 128 Mb of QSPI Flash
- MicroSD card interface

Communications

- 10/100/1000 Ethernet
- USB 2.0 OTG
- USB-UART

User I/O (via dual board-to-board connectors)

- 7Z010 Version
 - 108 User I/O (100 PL, 8 PS MIO)
 - PL I/O configurable as up to 48 LVDS pairs or 100 single-ended I/O
- 7Z020 Version
 - 123 User I/O (115 PL, 8 PS MIO)
 - PL I/O configurable as up to 55 LVDS pairs or 115 single-ended I/O

Other

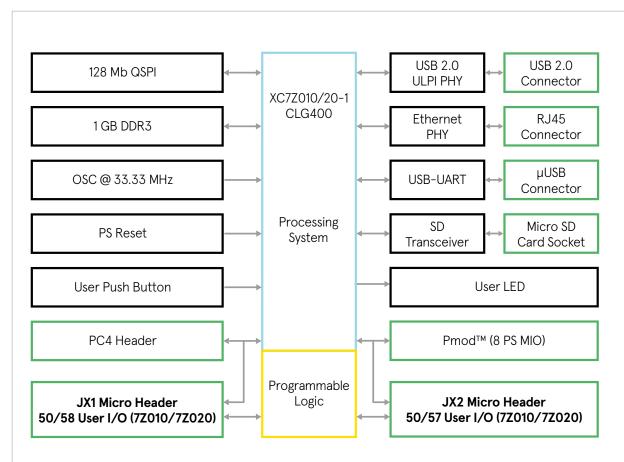
- 2x6 Digilent Pmod[®] compatible interface providing 8 PS MIO connections for user I/O
- Xilinx PC4 JTAG configuration port
- PS JTAG pins accessible via Pmod or I/O headers
- 33.33 MHz oscillator
- User LED and push button

Software

• Linux BSP and reference designs

Mechanical

4 inches x 2.25 inches (102 mm x 57 mm)



Additional information and downloadable documentation for MicroZed can be obtained at avnet.me/microzed

Part number	Description
AES-Z7MB-7Z010-SOM-G	7Z010 MicroZed SOM
AES-Z7MB-7Z010-SOM-I-G	7Z010 Ind. Temp MicroZed SOM
AES-Z7MB-7Z020-SOM-G	7Z020 MicroZed SOM
AES-Z7MB-7Z020-SOM-I-G	7Z020 Ind. Temp MicroZed SOM

Ultra96™

Ultra96[™] is an Arm-based, Xilinx Zynq UltraScale+[™] MPSoC development board based on the Linaro 96Boards specification. The 96Boards' specifications are open and define a standard board layout for development platforms that can be used by software application, hardware device, kernel, and other system software developers. Ultra96[™] represents a unique position in the 96Boards community with a wide range of potential peripherals and acceleration engines in the programmable logic that is not available from other offerings.

Ultra96™ boots from the provided Delkin 16 GB microSD card pre-loaded with Embedded Linux plus Enlightenment Desktop. Engineers have options of connecting to Ultra96™ through a Webserver using integrated wireless access point capability or to use the provided PetaLinux desktop environment which can be viewed on the integrated Mini DisplayPort video output. Multiple application examples and on-board development options are provided as examples.

Ultra96™ provides four user-controllable LEDs. Engineers may also interact with the board through the 96Boards-compatible low-speed and high-speed expansion connectors by adding peripheral accessories.

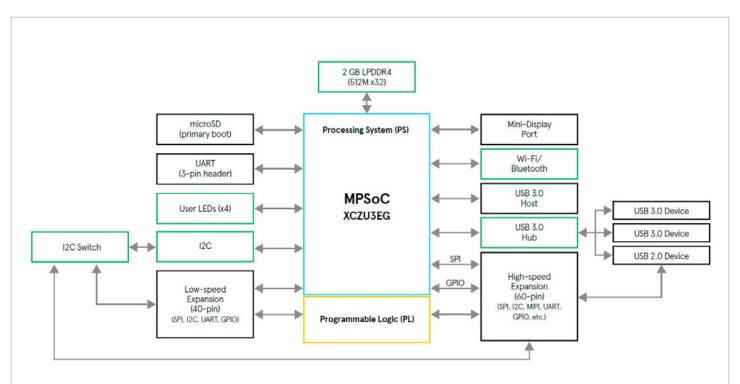


Micron LPDDR4 memory provides 2 GB of RAM in a 512M x 32 confi guration. Wireless options include 802.11b/g/n Wi-Fi and Bluetooth 4.2 (provides both Bluetooth Classic and Low Energy (BLE)). UARTs are accessible on a header as well as through the expansion connector. JTAG is available through a header (external USB-JTAG required). I2C is available through the expansion connector.

Ultra96™ provides one upstream (device) and two downstream (host) USB 3.0 connections. A USB 2.0 downstream (host) interface is provided on the highspeed expansion bus. Two Microchip USB3320 USB 2.0 ULPI Transceivers and one Microchip USB5744 4-Port SS/HS USB Controller Hub are specified. The integrated power supply generates all on-board voltages from an external 12V supply (available as an accessory).

FEATURES

- Xilinx Zynq UltraScale+ MPSoC ZU3EG A484
- Micron 2 GB (512M x32) LPDDR4 Memory
- Delkin 16 GB microSD card + adapter
 - Pre-loaded with Embedded Linux plus Enlightenment Desktop
- Wi-Fi / Bluetooth
- Mini DisplayPort (MiniDP or mDP)
- 1x USB 3.0 Type Micro-B upstream port
- 2x USB 3.0, 1x USB 2.0 Type A downstream ports
- 40-pin 96Boards Low-speed expansion header
- 60-pin 96Boards High-speed expansion header
- 85mm x 54mm form factor
- Linaro 96Boards Consumer Edition compatible



To purchase this kit, visit www.avnet.me/ultra96

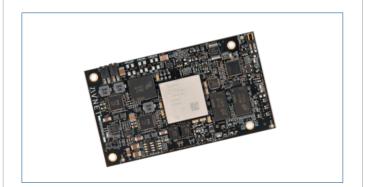
Part number	Description
AES-ULTRA96-G	Ultra96 Zynq UltraScale+ ZU3EG Development Board

Development Kits, Carrier Cards and Accessories



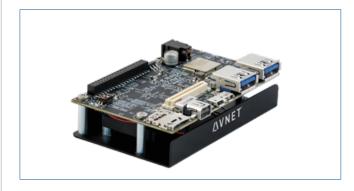
ULTRAZED-EV™

Part number	Description
AES-ZU7EV-1-SK-G	UltraZed-EV Starter Kit
AES-ZUEV-CC-G	UltraZed-EV Carrier Card



ULTRAZED-EG™

Part number	Description
AES-ZU3EG-1-SK-G	UltraZed-EG Starter Kit
AES-ZU-IOCC-G	UltraZed-EG IO Carrier Card
AES-ZU-PCIECC-G	UltraZed-EG PCIe Carrier Card



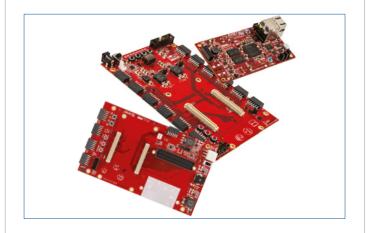
ULTRA96™

Part number	Description
AES-ULTRA96-G	Ultra96 Zynq UltraScale+ ZU3EG
	Development Board
AES-ACC-U96-JTAG	UART / JTAG cable
AES-ACC-U96-PWR	96Board Power 12V @ 2A
AES-ACC-U96-4APWR	96Board Power 12V @ 4A



ZYNQ® MINI-ITX™

Part number	Description
AES-MINI-ITX-7Z045-BAS-G	Z7045 Mini-ITX Base Kit
AES-MINI-ITX-7Z045-SYS-G	Z7045 Mini-ITX System Kit
AES-MINI-ITX-7Z100-SYS-G	Z7100 Mini-ITX System Kit



$MICROZED^{TM}$

Part number	Description
AES-Z7MB-7Z010-G	MicroZed Evaluation Kit
AES-MBCC-IO-G	I/O Carrier Card
AES-MBCC-FMC-G	FMC Carrier Card
AES-ARDUINO-CC-G	Arduino Carrier Card
AES-MBCC-BRK-G	Breakout Carrier Card

$\mathsf{PICOZED^{\mathsf{TM}}}$

Part number	Description
AES-PZCC-FMC-V2-G	PicoZed Carrier Card V2t

FMC

Part Number	Description
AES-FMC-NETW1-G	Network FMC
AES-FMC-MULTI-CAM4-G	Multicamera FMC
AES-FMC-HDMI-CAM-G	HDMI I/O FMC Module
AES-FMC-ISMNET2-G	ISM Networking FMC v2

OTHER KITS AND ACCESSORIES

Part Number	Description
AES-MINIZED-7Z007-G	MiniZed Z7007S Starter Kit
AES-Z7EV-7Z020-G	ZedBoard
AES-PMOD-TPM12-SLB9670-G	Infineon TPM v1.2 Peripheral Module
AES-CAM-ON-P1300C-G	PYTHON-1300C Module
AES-PMOD-TDM114-G	TDNext 1.26Mpixel Pmod Camera Kit
AES-PMOD-MUR-1DX-G	Murata 1DX Ble WiFi Bluetooth Pmod WiFi/BLE Module
210-299P-KIT	JTAG HS3 Programming Cable
AES-FMC-MC4-AR0231AT-G	Quad AR0231AT Camera FMC Bundle

Support

Our community-based site is dedicated to helping you jump-start your Xilinx Zynq®-7000 All Programmable SoCs and UltraScale+MPSoC projects. You'll find reference designs, documentation and training material supporting the platforms and solutions presented here. We hope you'll sign-on, join the community and get started today!

Reference Designs

Download the various reference designs and tutorials for any of the Zynq-based products.

Forums

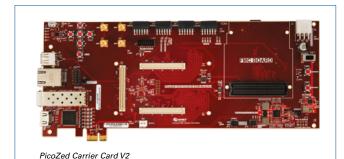
Ideas, questions and solutions from community members.

Training and Videos

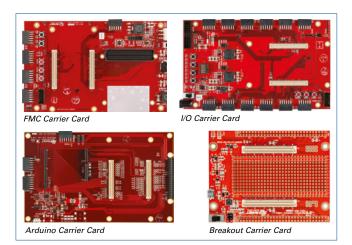
Learn how to create your own designs or see what others have done. You'll find introductory courses, advanced topics, architectural overviews and links to other resources.

System-on-Module Carrier Cards

/PICOZED™



/MICROZED™



FEATURES

Carrier Card V2

- microSD card socket
- x1 PCle Gen 2
- SFP+ cage
- USB-UART
- SMA ports
- 10/100/1000 Ethernet connector
- USB 2.0 connector
- LPC FMC Expansion
- Ethernet MAC ID (x2)
- High-performance transceiver clock
- Up to 4 $Pmod^{TM}$ expansion connectors

FEATURES

Breakout Carrier Card

• Two 60-pin (2x30) 0.1" footprints

FMC Carrier Card

- LPC FMC expansion connector
- Up to 4 Pmod[™] expansion connectors

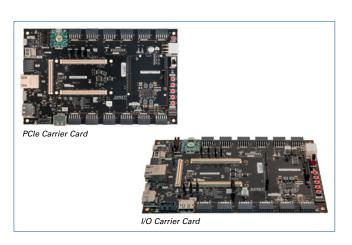
Arduino Carrier Card

Shield and Peripheral Module expansion (2x6 pin connectors)

I/O Carrier Card

Up to 12 Pmod[™] expansion connectors

/ULTRAZED-EG™



FEATURES

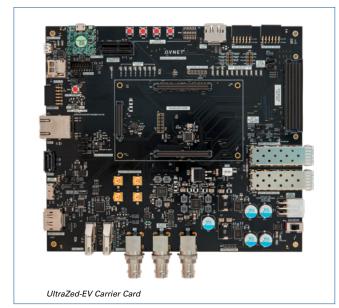
PCle Carrier Card

- LPC FMC Connector
- PCle x1Endpoint
- 1 PS Pmod
- 2 PL Pmods
- DP, Ethernet, USB
- SATA

IO Carrier Card

- Arduino Slot
- 1 PS Pmod
- 12 PL Pmods
- DP, Ethernet, USB
- SATA

/ULTRAZED-EV™



FEATURES

- Single UltraZed-EV SOM slot
- microSD card connector
- PS PMOD header
- Dual USB-UART
- DisplayPort connector
- USB 2.0/3.0 connector
- SATA 3.0 host interface
- PCle Gen2 x1 Root Port
- RJ45 connector
- 2 PL PMOD headers
- PL user DIP and Push switches

- PL user LEDs
- PS user LED
- PMBus header
- PS VBATT battery
- · SOM reset switch
- · Differential clock generator
- Digilent USB-JTAG module
- PC4 JTAG header
- I2C MAC Address device
- LVDS Touch Panel interface
- HDMI In/Out Interfaces
- 3G-SDI In/Out Interfaces
- Dual SFP+ interfaces
- FMC HPC slot
- 3 JX micro connectors (2 x 200-pin, 1 x 120-pin) providing the following connections to the UltraZed-EV SOM:
 - 152 user PL I/O pins
 - 6 user PS MIO pins (one full MIO bank)
 - 4 PS GTR transceivers
 - 4 PS GTR reference clock inputs
 - 16 PL GTH transceivers
 - 8 PL GTH reference clock inputs
 - PS JTAG interface
 - PL SYSMON interface
 - USB 2.0 connector interface
 - Gigabit Ethernet RJ45 connector interface
 - PMBus interface
 - SOM PS VBATT battery input
 - Carrier Card I2C interface
 - SOM Reset input, SOM Power Good output, and Carrier Card Reset output
 - Carrier Card interrupt input
 - Power and Ground pins

ABOUT EBV ELEKTRONIK

EBV Elektronik, an Avnet (Nasdaq:AVT) company, was founded in 1969 and is the leading specialist in EMEA semiconductor distribution. EBV maintains its successful strategy of personal commitment to customers and excellent services. 230 Technical Sales Specialists provide a strong focus on a selected group of long-term manufacturing partners. 110 continuously trained Application Specialists offer extensive application know-how and design expertise. With the EBVchips Program, EBV, together with its customers, defines and develops new semiconductor products. Targeted customers in selected growth markets will be supported by the Vertical Sales Segments. Warehouse operations, complete logistics solutions and value-added services such as programming, taping & reeling and laser marking are fulfilled by Avnet Logistics, EBV's logistical backbone and Europe's largest service centre. EBV operates from 64 offices in 29 countries throughout EMEA (Europe – Middle East – Africa). For more information about EBV Elektronik, please visit www.ebv.com.

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